



GLOBAL INSTITUTE OF ENGINEERING & TECHNOLOGY

(Approved by AICTE & Affiliated to JNTUH)

Survey No. 179, Chilkur (V), Moinabad (M), Ranga Reddy Dist. TS.

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JNTUH Code(U6) CIVIL – CSE – MECH – ECE – EEE – MBA – M.Tech. EAMCET Code-GLOB

Department of Electronics and Communication Engineering

G Ahmed Zeeshan

BE.,M.Tech.,(Ph.D)

MISTE.,MISOL.,MIEEEE(USA)

Assistant Professor & Head

Date: 13/12/2016

CIRCULAR

The Department of Electronics and Communication Engineering is hosting for *A Two Day Workshop on PCB Design & Fabrication* for the students of B. Tech.(ECE) in Room No. 04 on 05th January 2017 & 06th January 2017. All the students of B.Tech (ECE) are hereby informed to register for the course on or before 04th January 2017. The Co-Ordinator for the course is Ms.Nuzath Unnisa Begum, Assistant Professor, Department of ECE.

Zeeshan
13/12/2016
HEAD

HEAD

Department of Electronics & Communication Engg.
Global Institute of Engineering & Technology
Chilkur (V), Moinabad (M), R.R. Dist.TS.-501504

Cc to:

Principal

Co-ordinator IQAC

Mentors

Training & Placement Cell

Notice board

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

A Two Day Workshop

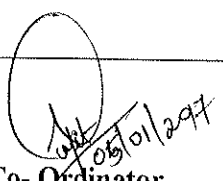
On

PCB Design & Fabrication

(05th & 06th January, 2017)

PROGRAMME SCHEDULE

	TIME	SESSION	SPEAKER
Day 1	9:30 AM - 10:00 AM	Inauguration	
	10:00 AM - 11:00 AM	Introduction to PCB	Mr. Ahmed Zeeshan, Asst. Professor ECE, GIET
	11:00 AM - 11:30 AM	HIGH TEA	
	11:30 AM - 01:00 PM	Types of PCB and design	Prof. Srinivasulu, Professor of ECE, CBIT
	01:00 PM - 02:00 PM	LUNCH BREAK	
	02:00 PM - 03:30 PM	Technologies, Materials, Software tools in PCB Design	Mr. Vinay Kumar Design Engineer, Nexoft Pvt. Ltd., Hyd.
Day 2	TIME	SESSION	SPEAKER
	09:30 AM - 11:00 AM	Fabrication Process of a PCB	Dr. Ravi Kumar, Professor of ECE, Siddhartha Inst. of Engg. & Tech.
	11:00 AM - 11:15 AM	TEA BREAK	
	11:15 AM - 01:00 PM	Multilayer PCB Applications	Dr. Ravi Kumar, Professor of ECE, Siddhartha Inst. of Engg. & Tech.
	01:00 PM - 02:00 PM	LUNCH BREAK	
	02:00 PM - 03:30 PM	Valedictory Function, Feedback and Certificate Distribution	


 Co-Ordinator
 Ms. Nuzath Unnisa Begur
 Assistant Professor
 Department of ECE



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DEPARTMENT OF ELECTRONICS AND COMMUNICATION
ENGINEERING

A Two Day Workshop

On

PCB DESIGN & FABRICATION

(05th & 06th January, 2017)

LIST OF PARTICIPANTS

S. NO:	NAME OF THE PARTICIPANT
1.	ALLU SOMESH
2.	BODDU DIVYA
3.	DEVISSETTY HEMANTH
4.	KADIRE PAVANI REDDY
5.	KAMBAM BHAVANI
6.	KARRA MANOJ
7.	KEDIKA SHUSHANTH REDDY
8.	KODALI SAI NAVEEN
9.	KODARI PRAVEEN REDDY
10.	KOLUGURI SREEJA
11.	KUNTE PRIYANKA
12.	LAKKIREDDY SRIKANTH REEDY
13.	MOHAMMED MOSAAD LABABIDI
14.	NYALAM BHANUCHANDAR
15.	RAGI MAHENDER
16.	SYED ATIF SADATH
17.	VINAY UNIYAL
18.	L MAHESH


Co-Ordinator

Ms. Nuzath Unnisa Begum

Assistant Professor

Department of ECE



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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

A Two Day Workshop
On

PCB Design & Fabrication
(05th & 06th January, 2017)

REGISTRATION FORM

(Please Fill in CAPITAL LETTERS)

Name : KALRA MANOJ

Roll No : 15UB1A0410

Course/Year : B. Tech 11YR

Department : ECE

E-mail : manojreddykerr@gmail.com

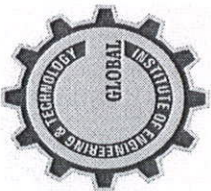
Phone No : 8186869730

manoj
Signature of Participant

For Office use only

Approved/ Not Approved

[Signature]
Signature of Coordinator



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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING A Two Day Workshop

On
PCB Design & Fabrication
 (05th & 06th January, 2017)
ATTENDANCE SHEET

S.No.	Name of the Participant	Programme	Year	Signature			
				Day-1 (05/01/2017)		Day-2 (06/01/2017)	
				FN	AN	FN	AN
1.	ALLU SOMESH	B.Tech	II	A.Somesh	A.Somesh	A.Somesh	A.Somesh
2.	BODDU DIVYA	B.Tech	II	Divya	Divya	Divya	Divya
3.	DEVISETTY HEMANTH	B.Tech	II	D.Hemant	D.Hemant	D.Hemant	D.Hemant
4.	KADIRE PAVANI REDDY	B.Tech	II	K.pavani	K.pavani	K.pavani	K.pavani
5.	KAMBAM BHAVANI	B.Tech	II	Bhavani	Bhavani	Bhavani	Bhavani

[Signature]
 Co Ordinator



[Signature]
 06/01/2017
 Head of the Department

[Signature]
 Principal
 Global Institute of Engg. & Tech
 Chilkur (V), Moinebad (M)
 R. R. Dist

Department of Electronics & Communication Engg.
 Global Institute of Engineering & Technology
 Chilkur (V), Moinebad (M), R.R. Dist T.S.-501504

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S.No.	Name of the Participant	Programme	Year	Signature			
				Day-1 (05/01/2017)		Day-2 (06/01/2017)	
				FN	AN	FN	AN
6.	KARRA MANOJ	B.Tech	II	Manoj	Manoj	Manoj	AN
7.	KEDIKA SHUSHANTH REDDY	B.Tech	II	Shushanth	Shushanth	Shushanth	AN
8.	KODALI SAI NAVEEN	B.Tech	II	Naveen	Naveen	Naveen	AN
9.	KODARI PRAVEEN REDDY	B.Tech	II	Praveen	Praveen	Praveen	AN
10.	KOLUGURI SREEJA	B.Tech	II	Sreeja	Sreeja	Sreeja	AN



05/01/2017
Co Ordinator

Praveen Reddy
HEAD
Department of Electronics & Communication Engg.
Head of the Department
Global Institute of Engineering & Technology
Chilkur (V), Moinebad (M), R.R. Dist. T.S.-501504

Principal
Global Institute of Engg. & Tech
Chilkur (V), Moinebad (M)
R. R. Dist

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S.No.	Name of the Participant	Programme	Year	Signature			
				Day-1 (05/01/2017)		Day-2 (06/01/2017)	
				FN	AN	FN	AN
11.	KUNTE PRIYANKA	B.Tech	II	<i>Priyanka Kunte</i>	<i>Priyanka Kunte</i>	<i>Priyanka Kunte</i>	<i>Priyanka Kunte</i>
12.	LAKKIREDDY SRIKANTH REEDY	B.Tech	II	<i>Srikanth Reedy</i>	<i>Srikanth Reedy</i>	<i>Srikanth Reedy</i>	<i>Srikanth Reedy</i>
13.	MOHAMMED MOSAAD LABABIDI	B.Tech	II	<i>Mosaad Lababidi</i>	<i>Mosaad Lababidi</i>	<i>Mosaad Lababidi</i>	<i>Mosaad Lababidi</i>
14.	NYALAM BHANUCHANDAR	B.Tech	II	<i>Nyalam Bhanuchandar</i>	<i>Nyalam Bhanuchandar</i>	<i>Nyalam Bhanuchandar</i>	<i>Nyalam Bhanuchandar</i>
15.	RAGI MAHENDER	B.Tech	II	<i>Ragi Mahender</i>	<i>Ragi Mahender</i>	<i>Ragi Mahender</i>	<i>Ragi Mahender</i>

(Signature)
Co Ordinator



(Signature)
06/01/2017

Head of the Department
Department of Electronics & Communication Engg.
Global Institute of Engineering & Technology
Chilkur (V), Moinabad (M), R.R. Dist. T.S. - 501504

(Signature)
Principal

Global Institute of Engg. & Tech
Chilkur (V), Moinabad (M)
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PCB Design & Fabrication
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ATTENDANCE SHEET

S.No.	Name of the Participant	Programme	Year	Signature						
				Day-1 (05/01/2017)		Day-2 (06/01/2017)				
				FN	AN	FN	AN	FN	AN	
16.	SYED ATIF SADATH	B.Tech	II	<i>Atif</i>	<i>Atif</i>	<i>Atif</i>	<i>Atif</i>	<i>Atif</i>	<i>Atif</i>	<i>Atif</i>
17.	VINAY UNIYAL	B.Tech	II	<i>Vinay</i>	<i>Vinay</i>	<i>Vinay</i>	<i>Vinay</i>	<i>Vinay</i>	<i>Vinay</i>	<i>Vinay</i>
18.	L MAHESH	B.Tech	II	<i>Maahesh</i>	<i>Maahesh</i>	<i>Maahesh</i>	<i>Maahesh</i>	<i>Maahesh</i>	<i>Maahesh</i>	<i>Maahesh</i>

[Signature]
Co Ordinator

[Signature]
Head of the Department
HEAD



[Signature]
Principal
PRINCIPAL
Global Institute of Engg. & Tech
Chilkur (V), Moinabad (M)
R. R. Dist

Department of Electronics & Communication Engg.
Global Institute of Engineering & Technology
Chilkur (V), Moinabad (M), R.R. Dist, T.S-501504



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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING A Two Day Workshop

On
PCB Design & Fabrication
(05th & 06th January, 2017)

FEEDBACK FORM

Please evaluate your rating of the course by placing a tick in the appropriate box.

1. Poor 2. Satisfactory 3. Good 4. Very good 5. Excellent

Branch and Year: B.Tech - 11 year

Date: 06/01/2017

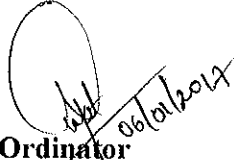
ASPECTS	RATING				
	Excellent 5	Very good 4	Good 3	Satisfactory 2	Poor 1
Relevance of contents	✓				
Trainer was knowledgeable and skillful	✓				
Quality of input provided	✓				
Quality of presentations	✓				
Adherence to the time schedule	✓				
Opportunity given to participant to clear doubts	✓				
Identify ways to build on current skills and knowledge	✓				
Overall learning experience	✓				
How has the course enhanced your skills or understanding of this topic?					
- No -					
Specify problems faced by you during the course?					
- No -					

REPORT
ON
TWO DAY WORKSHOP:
PCB DESIGN & FABRICATION
HELD AT
GLOBAL INSTITUTE OF ENGINEERING AND
TECHNOLOGY, HYDERABAD
(05th & 06th January, 2017)

A Two day Workshop titled “PCB Design & Fabrication” was organized by department of Electronics & Communication Engineering OF GIET, HYDERABAD the workshop commenced with inaugural ceremony at 9:30 AM Mr.G.Ahmed Zeeshan, Assistant Professor ECE, GIET. Welcomed all the dignitaries present on the dais and colleagues & students. He mentioned about the significance of the workshop. Prof. Srinivasulu, Professor of ECE, CBIT described the importance of PCB in electronics and communication engineering and how they are interconnected from a long time. He motivated everyone to actively participate in the workshop to get benefited.

Day 1: The Technical session started at 10:00 AM, Mr.G.Ahmed Zeeshan, Assistant Professor ECE, GIET. Delivered a topic on “Introduction to PCB”. He discussed about evolution of PCB and its various applications. Prof. Srinivasulu, Professor, CBIT explained the “Types of PCB and design” in which sir explained about various characteristics of PCB like substrate parameters, thickness etc. from 11:30 AM to 1:00 PM. The next speaker Mr. Vinay Kumar, Design Engineer, Nexoft Pvt.Ltd., Hyd lectured on “Technologies, Materials, Software tools in PCB Design” which includes different technologies used for designing of PCB. He also discussed the types of substrates used and dielectric constants and various tools from 2:00 PM to 3:30 PM.

Day 2: Session started at 9:30 am, Dr. Ravi Kumar, Professor of ECE, Siddhartha Inst. of Engg. & Tech. delivered two lectures on “Fabrication process of a PCB” & “Multilayer PCB Applications”. He discussed about PCB Fabrications steps and explained each step with its schematic diagram in first session. Followed by the Multilayer PCB and its uses in second session. Both the sessions gives hands on experience of PCB Fabrication process. The workshop concludes with validatory function, feedback and certificate distribution.


Co Ordinator
Ms.Nuzath Unnisa Begum
Assistant Professor
Department of ECE



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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

A Two Day Workshop

On

PCB DESIGN & FABRICATION

(05th & 06th January 2017)

CERTIFICATE OF PARTICIPATION

This is to certify that Ms./Mr. _____

ALLU SOMESE

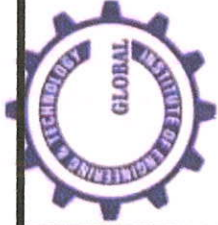
bearing roll number 15U61A0401 has attended a Two Day Workshop on "PCB DESIGN & FABRICATION"

held on 05th January 2017 & 06th January 2017 at Global Institute of Engineering and Technology.

Co-ordinator
(Nuzath Unnisa Begum)

Head of the Department
(G. Ahmed Zeeshan)

Principal
(Dr. V. V. V. S. Murthy)



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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

A Two Day Workshop

On

PCB DESIGN & FABRICATION

(05th & 06th January 2017)

CERTIFICATE OF PARTICIPATION

This is to certify that Ms./Mr. _____

K. PAVANI

bearing roll number 15UG1A0408 has attended a Two Day Workshop on "PCB DESIGN & FABRICATION"

held on 05th January 2017 & 06th January 2017 at Global Institute of Engineering and Technology.

Co-ordinator
(Nuzath Unnisa Begum)

Head of the Department
(G. Ahmed Zeeshan)

Principal
(Dr. V. V. V. S. Murthy)



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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

A Two Day Workshop

On

PCB DESIGN & FABRICATION

(05th & 06th January 2017)

CERTIFICATE OF PARTICIPATION

This is to certify that Ms./Mr. KAMBAM BHAYANI

bearing roll number 15UG1A0409 has attended a Two Day Workshop on "PCB DESIGN & FABRICATION"

held on 05th January 2017 & 06th January 2017 at Global Institute of Engineering and Technology.

Co-ordinator
(Nuzath Unnisa Begum)

Head of the Department
(G. Ahmed Zeeshan)

Principal
(Dr. V. V. S. Murthy)