



GLOBAL INSTITUTE OF ENGINEERING & TECHNOLOGY

(Approved by AICTE & Affiliated to JNTUH)

Survey No. 179, Chilkur (V), Moinabad (M), Ranga Reddy Dist. TS.

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JNTUH Code(U6) CIVIL – CSE – MECH – ECE – EEE – MBA – M.Tech. EAMCET Code-GLOB

Department of Electronics and Communication Engineering

G Ahmed Zeeshan

BE.,M.Tech.,(Ph.D)

MISTE.,MISOI.,MIEEE(USA)

Assistant Professor & Head

Date: 12/12/2017

CIRCULAR

The Department of Electronics and Communication Engineering is hosting for *A Two Day Workshop on PCB Design & Fabrication* for the students of B. Tech.(ECE) in Room No. 04 on 03rd January 2018 & 04th January 2018. All the students of B.Tech (ECE) are hereby informed to register for the course on or before 02nd January 2018. The Co-Ordinator for the course is Mrs.Ishrath Unisa, Assistant Professor, Department of ECE.

Zee
12/12/2017
HEAD

Department of Electronics & Communication Eng
Global Institute of Engineering & Technolog
Chilkur (V), Moinabad (M), R.R. Dist.T.S.-50150

Cc to:
Principal
Co ordinator IQAC
Mentors
Training & Placement Cell
Notice board
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SURVEY NO 179, CHILKUR(V), MOINABAD(M), RANGAREDDY DIST, T.S

DEPARTMENT OF ELECTRONICS AND COMMUNICATION
ENGINEERING

A Two Day Workshop

On

PCB Design & Fabrication

03rd & 04th January, 2018)

PROGRAMME SCHEDULE

	TIME	SESSION	SPEAKER
Day 1	9:30 AM - 10:00 AM	Inauguration	
	10:00 AM - 11:00 AM	Introduction to PCB	Mr. Ahmed Zeeshan, Asst. Professor ECE, GIET
	11:00 AM - 11:30 AM	HIGH TEA	
	11:30 AM - 01:00 PM	PCB Designing Process	Prof. Srinivasulu, Professor of ECE, CBIT
	01:00 PM - 02:00 PM	LUNCH BREAK	
	02:00 PM - 03:30 PM	Technologies, Materials, Software tools in PCB Design	Mr. Vinay Kumar Design Engineer, Nexoft Pvt. Ltd., Hyd.
Day 2	TIME	SESSION	SPEAKER
	09:30 AM - 11:00 AM	PCB Manufacturing Process	Dr. Ravi Kumar, Professor of ECE, Siddhartha Inst. of Engg. & Tech.
	11:00 AM - 11:15 AM	TEA BREAK	
	11:15 AM - 01:00 PM	Multilayer PCB	Dr. Ravi Kumar, Professor of ECE, Siddhartha Inst. of Engg. & Tech.
	01:00 PM - 02:00 PM	LUNCH BREAK	
02:00 PM - 03:30 PM	Valedictory Function , Feedback and Certificate Distribution		

Co- Ordinator
Mrs. Ishrath Unisa
Assistant Professor
Department of ECE



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DEPARTMENT OF ELECTRONICS AND COMMUNICATION
ENGINEERING

A Two Day Workshop


On

PCB DESIGN & FABRICATION

(03rd & 04th January, 2018)

LIST OF PARTICIPANTS

S. NO:	NAME OF THE PARTICIPANT
1.	VEM VAMSHI
2.	AFREEN BEGUM
3.	ASRA FARDEEN
4.	BADDAMOLLA SHIREESHA
5.	BOGULAPALLY KARTHIK KUMAR REDDY
6.	CHIDDANNA REVANTH
7.	DESIBOYINA BHARGAV SAI
8.	JANGAM VIKAS KUMAR
9.	KAUSER BEGUM
10.	MOHD KAZIM SHARIF
11.	N SOWMYA
12.	NALLA POOJA REDDY
13.	NANDAMURI NARENDRA
14.	PANDAOLA SHASHIKUMAR
15.	SHEELAM RAMAKRISHNA
16.	SUMMAIYA
17.	YANAGANDULA SURESH
18.	A YAMUNA
19.	KARUPURAM BHAVANA
20.	MOHAMMED ABDUL BARI KHAN
21.	POTTA RAGHAVENDER
22.	SANKOJI KRISHNAVENI
23.	SANTHAPURAM NIKHIL


Co-Ordinator
Mrs. Ishrath Unisa
Assistant Professor
Department of ECE



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DEPARTMENT OF ELECTRONICS AND COMMUNICATION
ENGINEERING

A Two Day Workshop

On

PCB Design & Fabrication

(03rd & 04th January, 2018)

REGISTRATION FORM

(Please Fill in CAPITAL LETTERS)

Name : KARUPURAM BHAVANA

Roll No : 17065A0402

Course/Year : B.TECH/II

Department : ECE

E-mail : Krishnamudhiraj1964@gmail.com

Phone No : 6302389431

K. Bhavane
Signature of Participant

For Office use only

Approved/ Not Approved

Signature of Coordinator



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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
 A Two Day Workshop

On
 PCB Design & Fabrication
 (03rd & 04th January, 2018)
 ATTENDANCE SHEET

S.No.	Name of the Participant	Programme	Year	Signature			
				Day-1 (03/01/2018)	AN	FN	AN
1.	VEM VAMSHI	B.Tech	II	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>
2.	AFREEN BEGUM	B.Tech	II	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>
3.	ASRA FARDEEN	B.Tech	II	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>
4.	BADDAMOLLA SHIREESHA	B.Tech	II	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>
5.	BOGULAPALLY KARTHIK KUMAR REDDY	B.Tech	II	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>

Co-Ordinator
[Signature]

Head of the Department
[Signature]
 Department of Electronics & Communication Engg.
 Global Institute of Engineering & Technology
 Chilkur (V), Moinebad (M), R.R. Dist.T.S.-501504.



Principal
[Signature]
 PRINCIPAL
 Global Institute of Engg. & Tech
 Chilkur (V), Moinebad (M)
 R. R. Dist



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S.No.	Name of the Participant	Programme	Year	Signature			
				Day-1 (03/01/2018)	AN	FN	AN
6.	CHIDDANNA REVANTH	B.Tech	II	Revanth	Revanth	Revanth	Revanth
7.	DESIBOYINA BHARGAV SAI	B.Tech	II	Sai	Sai	Sai	Sai
8.	JANGAM VIKAS KUMAR	B.Tech	II	Vik	Vik	Vik	Vik
9.	KAUSER BEGUM	B.Tech	II	Beegum	Beegum	Beegum	Beegum
10.	MOHD KAZIM SHARIF	B.Tech	II	Kazim	Kazim	Kazim	Kazim

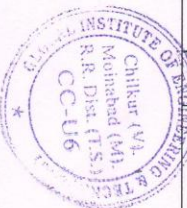
Co Ordinator

[Signature]

Head of the Department

[Signature]

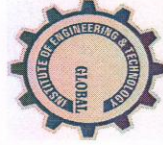
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Principal

[Signature]

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 (03rd & 04th January, 2018)

ATTENDANCE SHEET

S.No.	Name of the Participant	Programme	Year	Signature			
				Day-1 (03/01/2018)	AN	FN	AN
11.	N SOWMYA	B.Tech	II	MS.R	NSR	N.S.R	NS.R
12.	NALLA POOJA REDDY	B.Tech	II	N.pooja	N.pooja	N.pooja	N.pooja
13.	NANDAMURI NARENDRA	B.Tech	II	Part	Part	Part	Part
14.	PANDAOUILA SHASHIKUMAR	B.Tech	II	Shashi	Shashi	Shashi	Shashi
15.	SHEELAM RAMAKRISHNA	B.Tech	II	ela	ela	ela	ela

Co Ordinator

Head of the Department

Department of Electronics & Communication Engg.
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 DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

A Two Day Workshop
 On

PCB Design & Fabrication
 (03rd & 04th January, 2018)
 ATTENDANCE SHEET

S.No.	Name of the Participant	Programme	Year	Signature			
				Day-1 (03/01/2018)	Day-2 (04/01/2018)	FN	AN
16.	SUMMAIYA	B.Tech	II	<i>Summaya</i>	<i>Summaya</i>	<i>Summaya</i>	<i>Summaya</i>
17.	YANAGANDULA SURESH	B.Tech	II	<i>Shk</i>	<i>Shk</i>	<i>Shk</i>	<i>Shk</i>
18.	A YAMUNA	B.Tech	II	<i>A.yamuna</i>	<i>A.yamuna</i>	<i>A.yamuna</i>	<i>A.yamuna</i>
19.	KARUPURAM BHAVANA	B.Tech	II	<i>K.Bhavana</i>	<i>K.Bhavana</i>	<i>K.Bhavana</i>	<i>K.Bhavana</i>
20.	MOHAMMED ABDUL BARI KHAN	B.Tech	II	<i>Barikhan</i>	<i>Barikhan</i>	<i>Barikhan</i>	<i>Barikhan</i>

Mud
 Co Ordinator
 11/18

Head of the Department
 Department of Electronics & Communication Engg.
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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
 A Two Day Workshop

On
 PCB Design & Fabrication
 (03th & 04th January, 2018)
ATTENDANCE SHEET

S.No.	Name of the Participant	Programme	Year	Signature			
				Day-1 (03/01/2018)	Day-2 (04/01/2018)	FN	AN
21.	POTTA RAGHAVENDER	B.Tech	II	<i>Raghu</i>	<i>Raghu</i>	FN	AN
22.	SANKOJI KRISHNAVENI	B.Tech	II	<i>Raghu</i>	<i>Raghu</i>	FN	AN
23.	SANTHAPURAM NIKHIL	B.Tech	II	<i>Nikhil</i>	<i>Nikhil</i>	FN	AN

Co Ordinator

Head of the Department

Department of Electronics & Communication Engg.
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Department of Electronics and Communication Engineering

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

A Two Day Workshop

On

Machine Learning

(3rd & 4th January, 2018)

FEEDBACK FORM

Please evaluate your rating of the course by placing a tick in the appropriate box.

1. Poor 2. Satisfactory 3. Good 4. Very good 5. Excellent

Branch and Year: *ECE 2018*

Date: *4/1/18*


ASPECTS	RATING				
	Excellent 5	Very good 4	Good 3	Satisfactory 2	Poor 1
Relevance of contents	<input checked="" type="checkbox"/>				
Trainer was knowledgeable and skillful	<input checked="" type="checkbox"/>				
Quality of input provided	<input checked="" type="checkbox"/>				
Quality of presentations	<input checked="" type="checkbox"/>				
Adherence to the time schedule	<input checked="" type="checkbox"/>				
Opportunity given to participant to clear doubts	<input checked="" type="checkbox"/>				
Identify ways to build on current skills and knowledge	<input checked="" type="checkbox"/>				
Overall learning experience	<input checked="" type="checkbox"/>				
How has the course enhanced your skills or understanding of this topic?					
<i>Excellent</i>					
Specify problems faced by you during the course?					
<i>✓</i>					

REPORT
ON
TWO DAY WORKSHOP:
PCB DESIGN & FABRICATION
HELD AT
GLOBAL INSTITUTE OF ENGINEERING AND
TECHNOLOGY, HYDERABAD
(03rd & 04th January, 2018)

A Two day Workshop titled “PCB Design & Fabrication” was organized by department of Electronics & Communication Engineering OF GIET, HYDERABAD the workshop commenced with inaugural ceremony at 9:30 AM Mr.G.Ahmed Zeeshan, Assistant Professor ECE, GIET. Welcomed all the dignitaries present on the dais and colleagues & students. He mentioned about the significance of the workshop. Prof. Srinivasulu, Professor of ECE, CBIT described the importance of PCB in electronics and communication engineering and how they are interconnected from a long time. He motivated everyone to actively participate in the workshop to get benefited.

Day 1: The Technical session started at 10:00 AM, Mr.G.Ahmed Zeeshan, Assistant Professor ECE, GIET. Delivered a topic on “Introduction to PCB”. He discussed about evolution of PCB and its various applications. Prof. Srinivasulu, Professor, CBIT explained the “PCB Designing Process” in which sir explained about various designing methodologies of PCB from 11:30 AM to 1:00 PM. The next speaker Mr. Vinay Kumar, Design Engineer, Nexoft Pvt.Ltd., Hyd lectured on “Technologies, Materials, Software tools in PCB Design” which includes different technologies used for designing of PCB. He also discussed the types of substrates used and dielectric constants and various tools from 2:00 PM to 3:30 PM.

Day 2: Session started at 9:30 am, Dr. Ravi Kumar, Professor of ECE, Siddhartha Inst. of Engg. & Tech. delivered two lectures on “PCB Manufacturing process” & “Multilayer PCB”. He discussed about PCB Manufacturing steps and explained each step with its schematic diagram in first session. Followed by the Multilayer PCB and its uses in second session. Both the sessions gives hands on experience of PCB Fabrication process. The workshop concludes with validatory function, feedback and certificate distribution.


Co Ordinator
Mrs. Ishrath Unisa
Assistant Professor
Department of ECE



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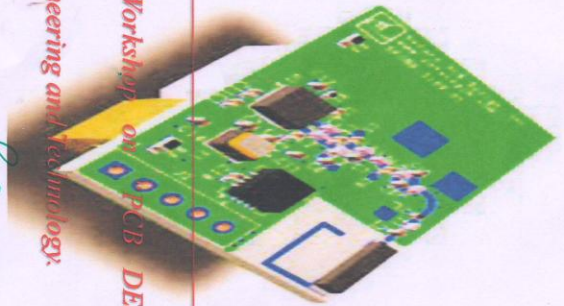
A Two Day Workshop
On
PCB DESIGN & FABRICATION
(03rd & 04th January 2018)


CERTIFICATE OF PARTICIPATION


This is to certify that Ms./Mr. KARUPURAM BHAVANA

bearing roll number 17U65A04102 has attended a Two Day Workshop on PCB DESIGN &

FABRICATION" held on 03rd January 2018 & 04th January 2018 at Global Institute of Engineering and Technology.




Co-Ordinator
(Ishrath Unisa)


Head of the Department
(G. Ahmed Zeeshan)


Principal
(Dr. V. V. V. S Murthy)



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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

A Two Day Workshop

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PCB DESIGN & FABRICATION

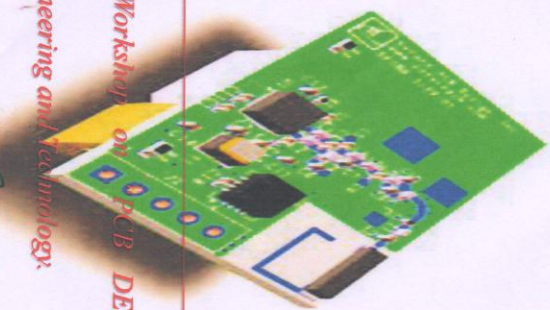
(03rd & 04th January 2018)

CERTIFICATE OF PARTICIPATION

This is to certify that Ms./Mr. A-FREEN BEGUM

bearing roll number 16U61A0401 has attended a Two Day Workshop on PCB DESIGN &

FABRICATION" held on 03rd January 2018 & 04th January 2018 at Global Institute of Engineering and Technology.



Ushra
Co-Ordinator
(Ushra Unisa)

Zeeshan
Head of the Department
(G. Ahmed Zeeshan)

Murthy
Principal
(Dr. V. V. S Murthy)



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A Two Day Workshop

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(03rd & 04th January 2018)

CERTIFICATE OF PARTICIPATION

This is to certify that Ms./Mr.

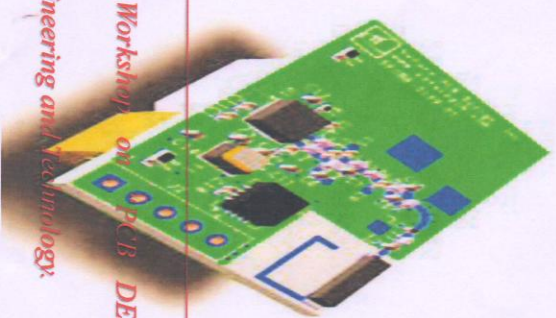
A YAMUNA

bearing roll number

17U65A0401

has attended a Two Day Workshop on PCB DESIGN &

FABRICATION" held on 03rd January 2018 & 04th January 2018 at Global Institute of Engineering and Technology.



[Signature]
Co-Ordinator
(Ishrath Unisa)

[Signature]
Head of the Department
(G. Ahmed Zeeshan)

[Signature]
Principal
(Dr. V. V. V. S Murthy)