



GLOBAL INSTITUTE OF ENGINEERING & TECHNOLOGY

(Approved by AICTE & Affiliated to JNTUH)

Survey No. 179, Chilkur (V), Moinabad (M), Ranga Reddy Dist. TS.

Phone: 08417-252233 / 253021

e-mail: principal.giet.u6@gmail.com

JNTUH Code(U6)
Code- GLOB

CIVIL - CSE - MECH - ECE - EEE - MBA - M.Tech.

EAMCET

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

G Ahmed Zeeshan
BE.,M.Tech.,(Ph.D)
MISTE.,MISOI.,MIEEEE(USA)
Assistant Professor & Head

Date: 12/08/2016

CIRCULAR

The Department of Electronics and Communication Engineering is hosting for *A Three Day Refresher Course on Basic Electronics & Circuit Designing* for the students of B. Tech in Room No. 04 from 18th August 2016 to 20th August 2016. All the students of B. Tech Electronics & Communication Engineering are hereby informed to register for the course on or before 12th August 2016. The Co-Ordinator for the course is Ms. Lakshmi Prasanna, Assistant Professor, Department of ECE.

Lakshmi Prasanna
12/08/2016
HEAD

HEAD

Department of Electronics & Communication
Global Institute of Engineering & Techno
Chilkur (V), Moinabad (M), R.R. Dist.T.S.:-

Cc to:
Principal
Co ordinator IQAC
Mentors
Training & Placement Cell
Notice board
File



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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

A Three Day Refresher Course

On

Basic Electronics & Circuit Designing

(18th – 20th August, 2016)

PROGRAMME SCHEDULE

	TIME/LENGTH	SESSION	SPEAKER
Day 1	09:30 AM - 10:00 AM	Inauguration	
	10:00 AM - 11:00 AM	Introduction to Electronics Components	Mr. G Ahmed Zeeshan Asst. Prof., & Head, GIET
	11:00 AM - 11:30 AM	Tea Break	
	11:30 AM - 01:00 PM	Identifying and Selecting Correct Components	Mrs. Ejaz Fathma, Assoc. Prof., Dept of EEE Muffakham Jah College of Engineering & Technology
	01:00 PM - 02:00 PM	Lunch Break	
	02:00 PM - 03:30 PM	Building a Simple Call Bell Circuit	Mr. Indranil Gupta, Technical Engineer, Coreel Technologies Pvt. Ltd, Hyderabad
Day 2	09:30 AM - 11:00 AM	Introduction to Transistors And Integrated Circuits And Bread Board Designing.	Mrs. Nuzath Unnisa Begum Asst Prof., GIET
	11:00 AM - 11:15 AM	Tea Break	
	11:15 AM - 01:00 PM	Designing of Water Level Indicator. Blinking Led Circuit	Mr. Ch Sreedher Assoc. Prof., & Head, Dept of ECE Nishitha College of Engineering & Technolog
	01:00 PM - 02:00 PM	Lunch Break	
Day 3	02:00 Pm - 03:30 Pm	Demonstration of the Buzzer Testing on Bread Board	Dr. S. Padam Priyal, Prof., GIET
	09:30 AM - 11:00 AM	Advanced Circuit Designing	Dr. C Shyam Anand, Prof., GIET
	11:00 AM - 11:15 AM	Tea Break	
	11:15 AM - 01:00 PM	Fire Alarm Using ICs, Siren (Ambulance, Police, Fire) using ICs.	Mr. Indranil Gupta, Technical Engineer, Coreel Technologies Pvt. Ltd, Hyderabad
	01:00 PM TO 02:00 PM	Lunch Break	
	02:00 PM TO 03:30 PM	Valedictory, Feedback and Certificate Distribution	

Lakshmi
Co-Ordinator
Ms. Lakshmi Prasanna
Assistant Professor
Department of ECE



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Department of Electronics and Communication Engineering
A Three Day Refresher Course
On
Basic Electronics & Circuit Designing
(18th – 20th August, 2016)
LIST OF PARTICIPANTS

S. NO	NAME OF THE PARTICIPANT
1.	ALLU SOMESH
2.	BODDU DIVYA
3.	DEVISSETTY HEMANTH
4.	KADIRE PAVANI REDDY
5.	KAMBAM BHAVANI
6.	KARRA MANOJ
7.	KEDIKA SHUSHANTH REDDY
8.	SAI NAVEEN
9.	KODARI PRAVEEN REDDY
10.	KOLUGURI SREEJA

Co-Ordinator *Lakshmi*
Ms. Lakshmi Prasanna
Assistant Professor
Department of ECE



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Basic Electronics & Circuit Designing

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LIST OF PARTICIPANTS

S. NO	NAME OF THE PARTICIPANT
11.	KUNTE PRIYANKA
12.	L. SRIKANTH REDDY
13.	MOHAMMED MOSAAD LABABIDI
14.	NYALAM BHANUCHANDAR
15.	SYED ATIF SADATH
16.	VEM VAMSHI
17.	VINAY UNIYAL

Lakshmi
18/8/16
Co-Ordinator
Ms.Lakshmi Prasanna
Assistant Professor
Department of ECE



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A Three Day Refresher Course

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(18th - 20th August, 2016)

REGISTRATION FORM

(Please Fill in CAPITAL LETTERS)

Name : ALLU SOMESH

Roll No : 15461A0401

Course/Year : B.Tech - II yr

Department : E.C.E

E-mail : Someshallu95@gmail.com

Phone No : 9642704295

A. Somesh

Signature of Participant

For Office use only

Approved/ Not Approved

Laxmi
Signature of Coordinator



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ATTENDANCE SHEET

S.No.	Name of the Participant	Programme	Year	Signature								
				Day-1 (18/08/2016)			Day-2 (19/08/2016)			Day-3 (20/08/2016)		
				FN	AN	AN	FN	AN	FN	AN	FN	AN
1.	ALLU SOMESH	B.TECH	II	A.Somesh	A.Somesh	A.Somesh	A.Somesh	A.Somesh	A.Somesh	A.Somesh	A.Somesh	A.Somesh
2.	BODDU DIVYA	B.TECH	II	B.Divya	B.Divya	B.Divya	B.Divya	B.Divya	B.Divya	B.Divya	B.Divya	B.Divya
3.	DEVISETTY HEMANTH	B.TECH	II	Hemant	Hemant	Hemant	Hemant	Hemant	Hemant	Hemant	Hemant	Hemant
4.	KADIRE PAVANI REDDY	B.TECH	II	K.Pavani	K.Pavani	K.Pavani	K.Pavani	K.Pavani	K.Pavani	K.Pavani	K.Pavani	K.Pavani
5.	KAMBAM BHAVANI	B.TECH	II	K.Bhavani	K.Bhavani	K.Bhavani	K.Bhavani	K.Bhavani	K.Bhavani	K.Bhavani	K.Bhavani	K.Bhavani

18/08/2016
 Co Ordinator

18/08/2016
 Head of the Department
 HEAD
 Department of Electronics & Communication Engg.
 Global Institute of Engineering & Technology
 Chilkur (V), Moimabad (M), R.R. Dist. T.S. 501504

18/08/2016
 Principal
 Global Institute of Engg. & Tech-
 Chilkur (V), Moimabad (M),
 R.R. Dist. T.S. 501504
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				Day-1 (18/08/2016)		Day-2 (19/08/2016)		Day-3 (20/08/2016)			
				FN	AN	FN	AN	FN	AN		
6.	KARRA MANOJ	B.TECH	II	(S)	(S)	(S)	(S)	(S)	(S)	(S)	(S)
7.	KEDIKA SHUSHANTH REDDY	B.TECH	II	Sushant	Sushant	Sushant	Sushant	Sushant	Sushant	Sushant	Sushant
8	SAI NAVEEN	B.TECH	II	Naveen	Naveen	Naveen	Naveen	Naveen	Naveen	Naveen	Naveen
9	KODARI PRAVEEN REDDY	B.TECH	II	Praveen	Praveen	Praveen	Praveen	Praveen	Praveen	Praveen	Praveen
10	KOLUGURI SREEJA	B.TECH	II	K. Sreeja	K. Sreeja	K. Sreeja	K. Sreeja	K. Sreeja	K. Sreeja	K. Sreeja	K. Sreeja

Co-Ordinator
 11/8/16



Head of the Department
 20/08/2016

Department of Electronics & Communication Engg.
 Global Institute of Engineering & Technology
 Chilkur (V), Moainabad (M), R.R. Dist.T.S.-501505.

Principal
 20/08/16

PRINCIPAL
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				Day-1 (18/08/2016)			Day-2 (19/08/2016)			Day-3 (20/08/2016)				
				FN	AN	AN	FN	AN	AN	FN	AN	AN		
11.	KUNTE PRIYANKA	B.TECH	II	Q	Q	Q	Q	Q	Q	Q	Q	Q	Q	Q
12.	L. SRIKANTH REDDY	B.TECH	II	Q	Q	Q	Q	Q	Q	Q	Q	Q	Q	Q
13.	MOHAMMED MOSAAD LABABIDI	B.TECH	II	Q	Q	Q	Q	Q	Q	Q	Q	Q	Q	Q
14.	NYALAM BHANUCHANDAR	B.TECH	II	Q	Q	Q	Q	Q	Q	Q	Q	Q	Q	Q
15.	SYED ATIF SADATH	B.TECH	II	Q	Q	Q	Q	Q	Q	Q	Q	Q	Q	Q

Co-Ordinator



Head of the Department

HEAD

Department of Electronics & Communication Engg.
 Global Institute of Engineering & Technology
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				Day-1 (18/08/2016)			Day-2 (19/08/2016)			Day-3 (20/08/2016)		
				FN	AN	FN	AN	FN	AN	FN	AN	
16.	VEM VAMSHI	B.TECH	II	<i>Vem Vamshi</i>	<i>Vem Vamshi</i>	<i>Vem Vamshi</i>	<i>Vem Vamshi</i>	<i>Vem Vamshi</i>	<i>Vem Vamshi</i>	<i>Vem Vamshi</i>	<i>Vem Vamshi</i>	
17.	VINAY UNIYAL	B.TECH	II	<i>Vinay Uniyal</i>	<i>Vinay Uniyal</i>	<i>Vinay Uniyal</i>	<i>Vinay Uniyal</i>	<i>Vinay Uniyal</i>	<i>Vinay Uniyal</i>	<i>Vinay Uniyal</i>	<i>Vinay Uniyal</i>	

Vem Vamshi
 Co Ordinator 16/8/2016



Vem Vamshi
 Head of the Department

Vem Vamshi
 20/08/2016

HEAD
 Department of Electronics & Communication Engg
 Global Institute of Engineering & Technology
 Chilkur (V), Moimabad (M), R.R. Dist.T.S.-501504

Vem Vamshi
 20/08/2016
 PRINCIPAL
 Global Institute of Engg. & Tech.
 Chilkur (V), Moimabad (M),
 R. R. Dist.



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FEEDBACK FORM

Please evaluate your rating of the course by placing a tick in the appropriate box.

1. Poor 2. Satisfactory 3. Good 4. Very good 5. Excellent

Branch and Year: *ECE II yr*

Date: *20/8/16*

ASPECTS	RATING				
	Excellent 5	Very good 4	Good 3	Satisfactory 2	Poor 1
Relevance of contents	<input checked="" type="checkbox"/>				
Trainer was knowledgeable and skillful	<input checked="" type="checkbox"/>				
Quality of input provided	<input checked="" type="checkbox"/>				
Quality of presentations	<input checked="" type="checkbox"/>				
Adherence to the time schedule		<input checked="" type="checkbox"/>			
Opportunity given to participant to clear doubts		<input checked="" type="checkbox"/>			
Identify ways to build on current skills and knowledge	<input checked="" type="checkbox"/>				
Overall learning experience	<input checked="" type="checkbox"/>				
How has the course enhanced your skills or understanding of this topic?					
<i>course was good.</i>					
Specify problems faced by you during the course?					
<i>-</i>					

A REPORT
ON
THREE DAY REFRESHER COURSE:
BASIC ELECTRONICS & CIRCUIT DESIGNING

A three day Refresher Course on "Basic Electronics & Circuit Designing" was organized during 18th August 2016 to 20th August 2016 in the college campus by the Department of ECE, Global Institute of Engineering and Technology. The Idea & Focus of this course for the students was to provide a deep understanding of concepts and to experience live circuit designs. The course was attended by seventeen students of B.Tech II Year (A.Y. 2016-17).

Day 1

The course started off with the inaugural ceremony and the Co ordinator of this occasion, Ms. Lakshmi Prasanna, Asst Prof, Department of ECE welcomed the gathering. Later in the inauguration our Resource Person, Mrs. Ejaz Fathma, Assoc. Prof., Dept of EEE Muffakham Jah College of Engineering & Technology & Mr. Ch Sreedher, Assoc. Prof., & Head, Dept of ECE Nishitha College of Engineering & Technology Hyderabad, delivered their inaugural and introductory speeches.

The technical session started immediately after inauguration, at 10.00 AM in which Mr. G Ahmed Zeeshan, Asst. Prof., & Head, GIET, delivered his enthusiastic talk on 'Introduction to Electronics Components'. He emphasized on the importance of today's electronics becoming more useful in day to day life. Mrs. Ejaz Fathma, Assoc. Prof., Dept of EEE Muffakham Jah College of Engineering & Technology explained the 'Identifying and Selecting Correct Components' with respect to the components specification & selection from 11:30 AM to 1:00 PM. From 2:00 PM to 3:30 PM Mrs. Ejaz Fathma, Assoc. Prof., Dept of EEE Muffakham Jah College of Engineering & Technology explained 'Building a Simple Call Bell Circuit' followed by a demo on proper connection.

Day 2

On second day, first session started at 9:30 AM by Mrs. Nuzath Unnisa Begum Asst Prof., GIET delivered a lecture on 'Introduction to Transistors and Integrated Circuits and Bread Board Designing' till 11:00 AM. Madam talked about the usage of transistor, designing of intergrated circuits and bread borad for creating creating temporary prototypes and experimenting with circuit design. It was followed by another lecture on 'Designing of Water Level Indicator, Blinking Led Circuit' by Mr. Ch Sreedher Assoc. Prof., & Head, Dept of ECE Nishitha College of Engineering & Technology, Hyderabad from 11:15 AM to 1:00 PM, in which sir guided the participants through the understanding of the LED Circuit and basic level of indicator. The next speaker Dr. S. Padam Priyal, Prof., GIET, gave hands on, 'Demonstration of the Buzzer Testing on Bread Board and Sample Demo' from 2:00 PM to 3:30 PM.

Day 3

On third day, first session started at 9:30 AM by a lecture on 'Advanced Circuit Designing' by Dr. C Shyam Anand, Prof., GIET. He spoke about advanced features of circuit designing to bridge the separation of introductory material on intergrated circuit analysis. The final session included a project demonstration on 'Fire Alarm Using ICs, Siren (Ambulance, Police, Fire) using ICs.' by Dr. S. Padam Priyal, Prof., GIET from 11:15 AM to 1:00 PM. All the students participated in the session actively participations & there was a good response from the students. The course was concluded by valedictory, followed by feedback and certificate distribution to the participants.

Laxmi
20/8/16
Co- Ordinator
Ms.LakshmiPrasanna
Assistant Professor
Department of ECE



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On


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(18th – 20th August 2016)

CERTIFICATE OF PARTICIPATION

This is to certify that Ms./Mr. ALLO SOMESH
bearing roll no 15U61A0401 has attended A Three Day Refresher Course on "Basic Electronics & Circuit Designing" held from 18th August 2016 to 20th August 2016 at Global Institute of Engineering and Technology.


Coordinator
(G.Laxmi)


Head of the Department
(G Ahmed Zeeshan)


Principal
(Dr. V. V. V. S. Murthy)



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CERTIFICATE OF PARTICIPATION

This is to certify that Ms./Mr. BODDU DIVYA
bearing roll no 15U61A0406 has attended A Three Day Refresher Course on "Basic Electronics & Circuit Designing" held from 18th August 2016 to 20th August 2016 at Global Institute of Engineering and Technology.


Coordinator
(G.Laxmi)


Head of the Department
(G Ahmed Zeeshan)


Principal
(Dr. V. V. V. S. Murthy)