



# GLOBAL Institute of Engineering & Technology

(Approved by AICTE & Affiliated to JNTUH)  
Survey No. 179, Chilukur (V), Moinabad (M), Ranga Reddy Dist. TS.

Phone: 8790101015/9959250205

e-mail: [principal.giet.u6@gmail.com](mailto:principal.giet.u6@gmail.com)

JNTUH Code(U6)

CIVIL - CSE - MECH - ECE - EEE - MBA - M.Tech.

EAMCET Code- GLOB

## Department of Electronics and Communication Engineering

**G Ahmed Zeeshan**

BE.,M.Tech.,(Ph.D)

MISTE.,MISOI.,MIEEE(USA)

Assistant Professor & Head

Date: 27/08/2018

### CIRCULAR

The Department of Electronics and Communication Engineering is hosting for *A Three Day Refresher Course on Analog Electronics and Circuit Design* for the students of B. Tech ECE in Room No. 04 from 27<sup>th</sup> September 2018 to 29<sup>th</sup> September 2018. All the students of B. Tech ECE are hereby informed to register for the course on or before 27<sup>th</sup> September 2018. The Co-Ordinator for the course is Mr. Sanka Ravi , Assistant Professor, Department of ECE.

Cc to:  
Principal  
Co ordinator IQAC  
Mentors  
Training & Placement Cell  
Notice board  
File

*Zeeshan*  
27/08/2018  
**HEAD**  
Depto. of Electronics & Communication Engg.  
Global Institute of Engineering & Technology  
Chilukur (V), Moinabad (M), R.R. Dist.T.S.-501504.

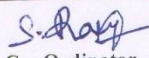


# GLOBAL Institute of Engineering & Technology

(Approved by AICTE & Affiliated to JNTUH)  
SURVEY NO 179, CHILKUR(V), MOINABAD(M), RANGAREDDY DIST, T.S

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING  
A Three Day Refresher Course  
On  
**Analog Electronics and Circuit Design**  
(27<sup>th</sup> to 29<sup>th</sup> September, 2018)  
**PROGRAMME SCHEDULE**

	TIME	SESSION	SPEAKER
Day 1	9:30 AM - 10:00 AM	Inauguration	
	10:00 AM - 11:00 AM	Introduction to Active & Passive Components	Mr. G Ahmed Zeeshan Asst. Prof. & Head Dept of ECE , GIET
	11:00 AM - 11:30 AM	HIGH TEA	
	11:30 AM - 01:00 PM	Distortion and Noise Analysis , Bias and Bias Stability	Mrs. Ishrath Unisa, Asst. Prof. , GIET
	01:00 PM - 02:00 PM	LUNCH BREAK	
	02:00 PM - 03:30 PM	Distortion and Noise Analysis, Bias and Bias Stability (Hands On)	Mrs. Ishrath Unisa, Asst. Prof. , GIET
Day 2			SPEAKER
	09:30 AM - 11:00 AM	Multiple Stages and Direct Coupling in Electronic Circuits	Dr. S Padam Priyal Prof. Dept of ECE , GIET
	11:00 AM - 11:15 AM	TEA BREAK	
	11:15 AM - 01:00 PM	Power Amplification in Analog Circuits	Mr. Ashok Chary Design Engineer , Pantech Solutions Pvt. Ltd , Hyderabad
	01:00 PM - 02:00 PM	LUNCH BREAK	
	02:00 PM - 03:30 PM	Direct Coupling in Electronic Circuits and Power Amplification in Analog Circuits (Hands On)	Mr. Ashok Chary Design Engineer , Pantech Solutions Pvt. Ltd , Hyderabad
Day 3			SPEAKER
	09:30 AM - 11:00 AM	Diodes and Basic BJT circuits	Mr. Ashok Chary Design Engineer , Pantech Solutions Pvt. Ltd , Hyderabad
	11:00 AM - 11:15 AM	TEA BREAK	
	11:15 AM - 01:00 PM	Diodes and Basic BJT circuits (Hands On)	Mr. Ashok Chary Design Engineer , Pantech Solutions Pvt. Ltd , Hyderabad
	01:00 PM - 02:00 PM	LUNCH BREAK	
	02:00 PM - 03:30 PM	Valedictory Function , Feedback and Certificate Distribution	

  
Co- Ordinator  
Mr. Sanka Ravi  
Assistant Professor  
Department of ECE





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## Department of Electronics and Communication Engineering

### A Three Day Refresher Course

On

### Analog Electronics & Circuit Design

(27<sup>th</sup>-29<sup>th</sup> September, 2018)

#### LIST OF PARTICIPANTS

S. NO:	NAME OF THE PARTICIPANT
1	Arachi Johnson
2	Devisetty Balaji
3	Gaddam Rakesh
4	Hareesh Reddy
5	Jana Sandhya
6	Janga Tharun Reddy
7	Jangatwar Tagore Pradeep Kumar
8	Kakarla Mrudula
9	Kancharwar Rahul Reddy
10	Kasari Uday Sagar
11	Lachireddy Bhagavathi

*S. Ravi*  
27/09/18  
Co-Ordinator  
Mr. Sanka Ravi  
Assistant Professor  
Department of ECE



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JNTUH Code(U6) CIVIL - CSE - MECH - ECE - EEE - MBA - M.Tech.

EAMCET Code- GLOB

**Department of Electronics and Communication Engineering**

**A Three Day Refresher Course**

**On**

**Analog Electronics & Circuit Design**

**(27<sup>th</sup>-29<sup>th</sup> September, 2018)**

**LIST OF PARTICIPANTS**

S. NO:	NAME OF THE PARTICIPANT
12	Mekala Nandu
13	Mohammed Asfiyan Ayub
14	Pakala Manoj Reddy
15	Racha Sruthi
16	Somireddy Bhargavi
17	Sunkari Sudheer
18	T. Divya Sri Durga
19	Thatipally Anwesh
20	Thatipally Jeevanajyothi
21	T Sree Alekhya
22	K.Durga Bhavani
23	MD Abdur Rahman

*S. Sanka Ravi*

**Co-Ordinator**  
Mr. Sanka Ravi  
Assistant Professor  
Department of ECE



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SURVEY NO 179, CHILKUR(V), MOINABAD(M), RANGAREDDY DIST, T.S

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

A Three Day Refresher Course

On

Analog Electronics & Circuit Design

(27<sup>th</sup> - 29<sup>th</sup> September, 2018)

**REGISTRATION FORM**

(Please Fill in CAPITAL LETTERS)

Name : D. BALAJI

Roll No : 17U6IA0403

Course/Year : B.TECH II

Department : ECF

E-mail : d.balaji1816@gmail.com

Phone No : 8523881816

*D. Balaji*  
Signature of Participant

For Office use only

Approved/ Not Approved

*C. Rang*  
Signature of Coordinator





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 SURVEY NO 179, CHILKUR(V), MOINABAD(M), RANGAREDDY DIST, T.S.

**Department of Electronics and Communication Engineering**

**A Three Day Refresher Course  
 On  
 "Analog Electronics and Circuit Design"  
 (27<sup>th</sup> - 29<sup>th</sup> September, 2018)  
 ATTENDANCE SHEET**

S.No.	Name of the Participant	Programme	Year	Signature										
				Day-1 (27/09/2018)			Day-2 (28/09/2018)			Day-3 (29/09/2018)				
				FN	AN	AN	FN	AN	AN	FN	AN	AN		
1	Arachi Johnson	B.Tech	II	Johnson Arachi	Johnson Arachi	Johnson Arachi	Johnson Arachi	Johnson Arachi	Johnson Arachi	Johnson Arachi	Johnson Arachi	Johnson Arachi	Johnson Arachi	Johnson Arachi
2	Devisetty Balaji	B.Tech	II	D Balaji	D Balaji	D Balaji	D Balaji	D Balaji	D Balaji	D Balaji	D Balaji	D Balaji	D Balaji	D Balaji
3	Gaddam Rakesh	B.Tech	II	G.Rakesh	G.Rakesh	G.Rakesh	G.Rakesh	G.Rakesh	G.Rakesh	G.Rakesh	G.Rakesh	G.Rakesh	G.Rakesh	G.Rakesh
4	Hareesh Reddy	B.Tech	II	Hareesh	Hareesh	Hareesh	Hareesh	Hareesh	Hareesh	Hareesh	Hareesh	Hareesh	Hareesh	Hareesh
5	Jana Sandhya	B.Tech	II	Jandhy Sandhya	Jandhy Sandhya	Jandhy Sandhya	Jandhy Sandhya	Jandhy Sandhya	Jandhy Sandhya	Jandhy Sandhya	Jandhy Sandhya	Jandhy Sandhya	Jandhy Sandhya	Jandhy Sandhya



27/09/2018  
 28/09/2018  
 29/09/2018

Principal  
 Global Institute of Engg. & Tech.  
 Chilkur (V), Moynabad (M), R.R. Dist. (T.S.)

Department Head of the Department  
 Global Institute of Engineering & Technology  
 Chilkur (V), Moynabad (M), R.R. Dist. (T.S.)

Co Ordinator



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SURVEY NO 179, CHILKUR(V), MOINABAD(M), RANGAREDDY DIST, T.S

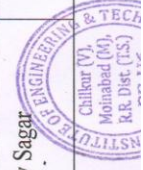
**Department of Electronics and Communication Engineering**

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S.No.	Name of the Participant	Programme	Year	Signature										
				Day-1 (27/09/2018)			Day-2 (28/09/2018)			Day-3 (29/09/2018)				
				FN	AN	AN	FN	AN	AN	FN	AN	AN		
6	Janga Tharun Reddy	B.Tech	II	Tharun	Tharun	Tharun	Tharun	Tharun	Tharun	Tharun	Tharun	Tharun	Tharun	Tharun
7	Jangatwar Tagore Pradeep	B.Tech	II	Pradeep	Pradeep	Pradeep	Pradeep	Pradeep	Pradeep	Pradeep	Pradeep	Pradeep	Pradeep	Pradeep
8	Kakarla Mrudula	B.Tech	II	Mrudula	Mrudula	Mrudula	Mrudula	Mrudula	Mrudula	Mrudula	Mrudula	Mrudula	Mrudula	Mrudula
9	Kancharwar Rahul Reddy	B.Tech	II	Rahul	Rahul	Rahul	Rahul	Rahul	Rahul	Rahul	Rahul	Rahul	Rahul	Rahul
10	Kasari Uday Sagar	B.Tech	II	Uday	Uday	Uday	Uday	Uday	Uday	Uday	Uday	Uday	Uday	Uday

C. Ravuri  
29/09/2018  
Co-Ordinator



Department of Electronics & Communication Engg.  
Global Institute of Engineering & Tech.

Principal  
Global Institute of Engineering & Tech.  
Principal

**Department of Electronics and Communication Engineering**

A Three Day Refresher Course

On

**“Analog Electronics and Circuit Design”**

(27<sup>th</sup> -29<sup>th</sup> September, 2018)

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S.No.	Name of the Participant	Programme	Year	Signature										
				Day-1 (27/09/2018)			Day-2 (28/09/2018)			Day-3 (29/09/2018)				
				FN	AN	AN	FN	AN	AN	FN	AN	AN		
11	Lachireddy Bhagavathi	B.Tech	II	L.Bh	L.Bh	L.Bh	L.Bh	L.Bh	L.Bh	L.Bh	L.Bh	L.Bh	L.Bh	L.Bh
12	Mekala Nandu	B.Tech	II	M.Nandu	M.Nandu	M.Nandu	M.Nandu	M.Nandu	M.Nandu	M.Nandu	M.Nandu	M.Nandu	M.Nandu	M.Nandu
13	Mohammed Asfiyan Ayub	B.Tech	II	M.Ayub	M.Ayub	M.Ayub	M.Ayub	M.Ayub	M.Ayub	M.Ayub	M.Ayub	M.Ayub	M.Ayub	M.Ayub
14	Pakala Manoj Reddy	B.Tech	II	M.Reddy	M.Reddy	M.Reddy	M.Reddy	M.Reddy	M.Reddy	M.Reddy	M.Reddy	M.Reddy	M.Reddy	M.Reddy
15	Racha Sruthi	B.Tech	II	S.Racha	S.Racha	S.Racha	S.Racha	S.Racha	S.Racha	S.Racha	S.Racha	S.Racha	S.Racha	S.Racha

Co Ordinator  
 S. R. V. Reddy  
 Chilkur (V),  
 Moinabad (M),  
 R.R. Dist. (T.S.)  
 CC-UG



Department of  
 Head of the Department  
 Global Institute of Engineering & Technology  
 Chilkur (V), Moinabad (M), R.R. Dist. (T.S.)

Principal  
 Global Institute of Engineering & Technology  
 Chilkur (V), Moinabad (M), R.R. Dist. (T.S.)





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Department of Electronics and Communication Engineering

A Three Day Refresher Course  
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ATTENDANCE SHEET

S.No.	Name of the Participant	Programme	Year	Signature										
				Day-1 (27/09/2018)			Day-2 (28/09/2018)			Day-3 (29/09/2018)				
				FN	AN	AN	FN	AN	AN	FN	AN	AN		
16	Somireddy Bhargavi	B.Tech	II	S	S	S	S	S	S	S	S	S	S	S
17	Sunkari Sudheer	B.Tech	II	S	S	S	S	S	S	S	S	S	S	S
18	T. Divya Sri Durga	B.Tech	II	T	T	T	T	T	T	T	T	T	T	T
19	Thatipally Anvesh	B.Tech	II	A	A	A	A	A	A	A	A	A	A	A
20	Thatipally Jeevanjayothi	B.Tech	II	J	J	J	J	J	J	J	J	J	J	J

*E. Naveen Kumar*  
Co Ordinator



Department of Electronics & Communication Engg  
Global Institute of Engineering & Technology

Principal  
Department of Electron. & Techn.

*M. Srinivas Reddy*



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**Department of Electronics and Communication Engineering**

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S.No.	Name of the Participant	Programme	Year	Signature					
				Day-1 (27/09/2018)		Day-2 (28/09/2018)		Day-3 (29/09/2018)	
				FN	AN	FN	AN	FN	AN
21	T Alekhya	B.Tech	II	<i>Start</i>	<i>Start</i>	<i>Start</i>	<i>Start</i>	<i>Start</i>	<i>Start</i>
22	K Durga Bhavani	B.Tech	II	<i>K Durga Bhavani</i>	<i>K Durga Bhavani</i>	<i>K Durga Bhavani</i>	<i>K Durga Bhavani</i>	<i>K Durga Bhavani</i>	<i>K Durga Bhavani</i>
23	MD Abdur Rahman	B.Tech	II	<i>MD Abdur Rahman</i>	<i>MD Abdur Rahman</i>	<i>MD Abdur Rahman</i>	<i>MD Abdur Rahman</i>	<i>MD Abdur Rahman</i>	<i>MD Abdur Rahman</i>

*S. Praveen Kumar*  
 Co Ordinator

*Start 27/09/2018*  
 Head of the Department  
 HEAD

*Principal*  
 Principal  
 PRINCIPAL  
 Global Institute of Engg. & Tech  
 Chilkur (V), Moinabad (M)  
 R. R. Dist.

Department of Electronics & Communication Engg.  
 Global Institute of Engineering & Technology  
 Chilkur (V), Moinabad (M), R.R. Dist.T.S.-501504





# GLOBAL Institute of Engineering & Technology

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SURVEY NO 179, CHILKUR(V), MOINABAD(M), RANGAREDDY DIST.T.S

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

## A Three Day Refresher Course On Analog Electronics and Circuit Design (27<sup>th</sup> - 29<sup>th</sup> September, 2018)

### FEEDBACK FORM

Please evaluate your rating of the course by placing a tick in the appropriate box.  
1. Poor 2. Satisfactory 3. Good 4. Very good 5. Excellent

Branch and Year: ECE 3<sup>rd</sup> II

Date: 29<sup>th</sup> Sep - 2018

ASPECTS	RATING				
	Excellent 5	Very good 4	Good 3	Satisfactory 2	Poor 1
Relevance of contents	<input checked="" type="checkbox"/>				
Trainer was knowledgeable and skillful	<input checked="" type="checkbox"/>				
Quality of input provided	<input checked="" type="checkbox"/>				
Quality of presentations	<input checked="" type="checkbox"/>				
Adherence to the time schedule	<input checked="" type="checkbox"/>				
Opportunity given to participant to clear doubts	<input checked="" type="checkbox"/>				
Identify ways to build on current skills and knowledge	<input checked="" type="checkbox"/>				
Overall learning experience	<input checked="" type="checkbox"/>				
How has the course enhanced your skills or understanding of this topic? <u>- Good</u>					
Specify problems faced by you during the course? <u>- NO -</u>					
Any Other Comments: <u>- NO -</u>					

**A REPORT**  
**ON**  
**THREE DAY REFRESHER COURSE:**  
**ANALOG ELECTRONICS & CIRCUIT DESIGN**

A 3-day Refresher Course on **Analog Electronics & Circuit Design** was organized by the Department of Electronics and Communication Engineering . The Refresher Course was held in the Conference Room of GIET from 27<sup>th</sup> to 29<sup>th</sup> September, 2018 and its objective was to further enhance and strengthen the technical skills of the students in the field of analog electronics. The course was attended by 23 students of II Year B. Tech (A.Y. 2018-19). Evaluation forms were also filled by each participant at the end of the course.

**Day 1:**

The inauguration of the 3-day refresher course was chaired by Mr. G Ahmed Zeeshan Asst. Prof. & Head Dept of ECE , GIET, he welcomed the gathering. Sir has emphasized the importance of the refresher course for the participants in view of the growing importance of analog electronics. Sir expressed his expectations from the students participating in course, to utilize the knowledge effectively & fully from the resource person as it will improve their skills. Furthermore, He expressed profound thanks and appreciation to the Management of GIET for its financial support. A PowerPoint presentation on the basic concepts of active & passive components had also been given from 9:30AM - 11:00AM. The next speaker was Mrs. Ishrath Unisa Asst. Professor, lectured on 'Distortion and Noise Analysis , Bias and Bias Stability', from 11:30 AM to 1:00PM. In this session she explained about distortion in signals ,biasing and stability in circuits. Last session for the day was hands on, on Distortion and Noise Analysis from 2.00PM to 3.00PM.



**Day 2:**

On second day, first session was taken by Mrs. Dr. S Padam Priyal Professor Dept of ECE, GIET, madam delivered a lecture on Multiple Stages and Direct Coupling in Electronic Circuits from 9:30 AM to 11:00AM. It was followed by another lecture on 'Power Amplification in Analog Circuits' by Mr. Ashok Chary, Design Engineer, Pantech Solutions Pvt. Ltd., Hyderabad, from 11:15 AM to 1:00 PM, in which he elaborated on various power amplification techniques. Last session of the day's was hands on, on Direct Coupling in Electronic Circuits & Power Amplification taken by sir from 2:00 PM - 3:30 PM .

**Day 3:**

On third day, first session started at 9:30 AM on 'Diodes and Basic BJT Circuits' by Mr. Ashok Chary sir. In which sir spoke about semiconductor devices like diodes and transistors and its applications. In the next session sir has explained about practical applications and construction of circuits using diodes and Bipolar Junction Transistors which started from 11:15AM and ended at 1:00PM. All the students participated in the sessions actively and have shown enthusiasm. The course was concluded by valedictory followed by feedback and certificate distribution to the participants.



**Co- Ordinator**  
Mr. Sanka Ravi  
Asstistant Professor  
Department of ECE



# GLOBAL Institute of Engineering & Technology

**PANTECH SOLUTIONS**  
Technology Beyond the Dreams

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**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**

*A Three Day Refresher Course*

*On*

*Analog Electronics & Circuit Design*

*(27<sup>th</sup> – 29<sup>th</sup> September 2018)*

**CERTIFICATE OF PARTICIPATION**

This is to certify that Ms./Mr. K. MRUDULA  
bearing roll no 17V61A0409 has attended A Three Day Refresher Course on "Analog Electronics and Circuit Design" held from 27<sup>th</sup> September 2018 to 29<sup>th</sup> September 2018 at Global Institute of Engineering and Technology.

**Co Ordinator**  
(Sanka Ravi)

**Head of the Department**  
(G Ahmed Zeeshan)

**Principal**  
(Dr. V. V. S. Murthy)





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**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**

*A Three Day Refresher Course*


*On*


*Analog Electronics & Circuit Design*


*(27<sup>th</sup> – 29<sup>th</sup> September 2018)*

**CERTIFICATE OF PARTICIPATION**

This is to certify that Ms./Mr. JANA SANDHYA  
bearing roll no 17U61A0406 has attended A Three Day Refresher Course on “Analog Electronics and Circuit Design” held from 27<sup>th</sup> September 2018 to 29<sup>th</sup> September 2018 at Global Institute of Engineering and Technology.

  
**Co Ordinator**  
**(Sanka Ravi)**

  
**Head of the Department**  
**(G Ahmed Zeeshan)**

  
**Principal**  
**(Dr.. V. V. V. S. Murthy)**



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**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**

*A Three Day Refresher Course*

*On*


*Analog Electronics & Circuit Design*

*(27<sup>th</sup> – 29<sup>th</sup> September 2018)*

**CERTIFICATE OF PARTICIPATION**

This is to certify that Ms./Mr. HAREESH REDDY  
bearing roll no 17UG1A0405 has attended A Three Day Refresher Course on “Analog Electronics and Circuit Design” held from 27<sup>th</sup> September 2018 to 29<sup>th</sup> September 2018 at Global Institute of Engineering and Technology.

  
**Co Ordinator**  
**(Sanka Ravi)**

  
**Head of the Department**  
**(G Ahmed Zeeshan)**

  
**Principal**  
**(Dr. V. V. S. Murthy)**





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## DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

*A Three Day Refresher Course*

*On*

*Analog Electronics & Circuit Design*

*(27<sup>th</sup> – 29<sup>th</sup> September 2018)*

### CERTIFICATE OF PARTICIPATION

This is to certify that Ms./Mr. G. RAKESH  
bearing roll no 17UG1A0404 has attended A Three Day Refresher Course on “Analog Electronics and Circuit Design” held from 27<sup>th</sup> September 2018 to 29<sup>th</sup> September 2018 at Global Institute of Engineering and Technology.

**Co Ordinator**  
**(Sanka Ravi)**

**Head of the Department**  
**(G Ahmed Zeeshan)**

**Principal**  
**(Dr. V. V. V. S. Murthy)**