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# JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD (Established by Andhra Pradesh Act No.30 of 2008) Kukatpally, Hyderabad – 500 085, Andhra Pradesh (India)

## **B. TECH. ELECTRONICS AND COMMUNICATION ENGINEERING**

#### I YEAR

Code	Subject	L	T/P/D	С
	English	2	-	4
	Mathematics – I	3	-	6
	Mathematical Methods	3	-	6
	Engineering Physics	3	-	6
	Engineering Chemistry	3	-	6
	Computer Programming	3	-	6
	Engineering Drawing	3	3	6
	Computer Programming Lab.	-	3	4
	Engineering Physics / Engineering Chemistry Lab.	-	3	4
	English Language Communication Skills Lab.	-	3	4
	IT Workshop / Engineering Workshop	-	3	4
	Total	20	15	56

#### **II YEAR I SEMESTER**

Code	Subject	L	T/P/D	С
	Mathematics - III	4	-	4
	Probability Theory and Stochastic Processes	4	-	4
	Switching Theory and Logic Design	4	-	4
	Electrical Circuits	4	-	4
	Electronic Devices and Circuits	4	-	4
	Signals and Systems	4	-	4
	Electronic Devices and Circuits Lab.	-	3	2
	Basic Simulation Lab.	-	3	2
	Total	24	6	28

## **II YEAR II SEMESTER**

Code	Subject	L	T/P/D	С
	Principles of Electrical Engineering	4	-	4
	Electronic Circuit Analysis	4	-	4
	Pulse and Digital Circuits	4	-	4
	Environmental Studies	4		4
	Electromagnetic Theory and Transmission Lines	4	-	4
	Digital Design using Verilog HDL	4	-	4
	Electrical Technology Lab.	-	3	2
	Electronic Circuits and Pulse Circuits Lab.	-	3	2
	Total	24	6	28

#### **III YEAR I SEMESTER**

Code	Subject	L	T/P/D	С
	Control Systems Engineering	4	-	4
	Computer Organization and Operating Systems	4	-	4
	Antennas and Wave Propagation	4	-	4
	Electronic Measurements and Instrumentation	4	-	4
	Analog Communications	4	-	4
	Linear and Digital IC Applications	4	-	4
	Analog Communications Lab.	-	3	2
	IC Applications and HDL Simulation Lab.	-	3	2
	Total	24	6	28

## III YEAR II SEMESTER

Code	Subject	L	T/P/D	С
	Managerial Economics and Financial Analysis	4	-	4
	Open Elective:	4	-	4
	Human Values and Professional Ethics			
	Disaster Management			
	Intellectual Property Rights			
	Digital Communications	4	-	4
	VLSI Design	4	-	4
	Microprocessors and Microcontrollers	4	-	4
	Digital Signal Processing	4	-	4
	Microprocessors and Microcontrollers Lab.	-	3	2
	Digital Signal Processing Lab.	-	3	2
	Total	24	6	28

# IV YEAR I SEMESTER

Code	Subject	L	T/P/D	С
	Management Science	4	-	4
	Microwave Engineering	4	-	4
	Computer Networks	4	-	4
	Cellular and Mobile Communications	4	-	4
	Elective -I: Digital Image Processing Multimedia and Signal Coding Object Oriented Programming through Java	4	-	4
	Elective -II: Television Engineering Optical Communications Embedded Systems Design	4	-	4
	Advanced English Language Communication Skills Lab,	-	3	2
	Microwave Engineering and Digital Communications Lab	-	3	2
	Total	24	6	28

## IV YEAR II SEMESTER

Code	Subject	L	T/P/D	С
	Elective -III:	4	-	4
	Satellite Communications			
	Biomédical Instrumentation			
	Artificial Neural Networks			
	Elective -IV:	4	-	4
	Telecommunication Switching Systems and			
	Networks			
	Radar Systems			
	Network Security			
	Elective -V:	4	-	4
	Wireless Communications and Networks			
	Digital Signal Processors and Architectures			
	RF Circuit Design			
	Industry Oriented Mini Project	-	-	2
	Seminar	-	6	2
	Major Project	-	15	10
	Comprehensive Viva	-	-	2
	Total	12	21	28

Note:All End Examinations (Theory and Practical) are of three hours duration.T-TutorialL – TheoryP – Practical/DrawingC – Credits