GLOBAL
Institute of Engineering & Technology
(Approved by AICTE & Affiliated to JNTUH)
Survey No. 179, Chilkur (V), Moinabad (M), Ranga Reddy Dist. TS.
Phone: 08417-252233 / 253021
e-mail: principal.giet.u6@gmail.com
JNTUH Code(U6) CIVIL – CSE – MECH – ECE – EEE – MBA – M.Tech. EAMCET Code GLOB
Department of Electronics & Communication Engineering

G Ahmed Zeeshan
B.E , M.Tech.,(ph.D)
Assistant Professor & Head

Lr.No: GIET/ECE/Compt Ex/001/09/2019-20

Date 09/09/2019

To

The Principal
Global Institute of Engineering &Technology
Moinabad

Sir,

Sub: Permission to Conduct Competetive Exam Coaching for Students of ECE-III &IV Yr

*****

This is to get it to your kind notice that we Department of Electronics & Communication Engineering are interested to conduct a Coaching for competetive exams to the students of III & IV year, which is undertaken for students benefit as a value addition to curriculum of course.

These Coaching classes help students to strengthen their basics of courses learnt from first year to final year and help them to crack various competetive exams.

Kindly accord approval so as to make necessary arrangements for starting the classes for students of Electronics & Communication Engineering– III & IV yr Academic Year: 2019-20.

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Thanking you.

HEAD

Permitted

HEAD
Department of Electronics & Communication Engg.
Global Institute of Engineering & Technology
Chilkur (V), Moinabad (M), R.R. Dist. T.S.-501504

Cc to :

Director- *for information*

Dean – *for information*

IQAC– *for information*

` **Heads** – **CE,EEE,ME,ECE,CSE** - *for information*

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Department of Electronics And Communication Engineering

G.Ahmed Zeeshan
BE , M.Tech., (ph.D)
Assistant Professor & Head

DEPARTMENT CIRCULAR

Cr.No: GIET/ECE/Compt Ex/004/09/2019-20

Date 21/09/2019

Department of Electronics And Communication Engineering is conducting Competetive Exam Coaching classes for Students of B. Tech ECE-III & IV Yr. from **23/09/2019**, in association with Sharada Coaching Academy- Hyderabad in Room No.13. The Co-Ordinator for the Competetive Exam Coaching classes is Ms. Imrose Basha, Assistant Professor, Department of ECE. Students who are interested can contact the coordinator and give your consent by tomorrow.

Head,
Dept of ECE.

HEAD
Department of Electronics & Communication Engg.
Global Institute of Engineering & Technology
Chilkur (V), Moinabad (M), R.R. Dist. T.S.-501509

Cc to :

ECE Department – *for filing & Display on Department Notice Board*

Mentors ECE-III&IV – *for informing students and post message in Class Group*

Coordinator- *for information*

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Department of Electronics & Communication Engineering

G.Ahmed Zeeshan
B.E, M.Tech (ph.D)
Assistant Professor & Head

DEPARTMENT CIRCULAR

Cr.No: GIET/ECE/Compt Ex/005/01/2019-20

Date 25/01/2020

Upon Successful Completion of Competitive Examinations Training for the current Academic Year, it is hereby informed that GATE/PGECET Mock Exam will be conducted on 01/02/2020 (FN) and TOEFLE/PTE/IELTS Mock Exam will be conducted on 01/02/2020 (AN) . In this regard all the students of B. Tech ECE- III & IV Year are here by informed to attend Examination.

For further details contact Competitive Examinations Co-Ordinator.

Head,
Dept of ECE.

HEAD
Department of Electronics & Communication Engg.
Global Institute of Engineering & Technology
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ECE Department – *for filing & Display on Department Notice Board*

Mentors ECE-III & IV – *for informing students and post message in Class Group*

Coordinator- *for information*

SHARADHA
COACHING ACADEMY

GLOBAL
Institute of Engineering &Technology

MEMORANDUM OF UNDERSTANDING

Between

GLOBAL INSTITUTE OF ENGINEERING &TECHNOLOGY

(Approved by AICTE & Affiliated to JNTUH)

Survey No. 179, Chilkur (V), Moinabad (M), Ranga Reddy Dist. TS.

&

SHARADHA
COACHING ACADEMY

Hyderabad

6-9-12/16A, Ramachandrapuram colony, IS Sadan, Saidabad,Hyderabad-500059

INSTITUTE OF ENGINEERING & TECHNOLOGY
Chilkur (V).
Moinabad (M).
R.R. Dist. (T.S.)
CC-U6

SHARADHA
COACHING ACADEMY

GLOBAL
Institute of Engineering &Technology

MEMORANDUM OF UNDERSTANDING

Between

Global Institute of Engineering & Technology, Moinabad

&

Sharadha Coaching Academy, Hyderabad

This Memorandum of understanding ("MOU") is entered into on this date **19th AUGUST 2017** by and between Global Institute of Engineering & Technology, Moinabad and Sharadha Coaching Academy, Hyderabad (**ACADEMY** and **COLLEGE** are here in after referred to individually as "Party" or collectively as "Parties").

This Memorandum of Understanding has been drawn up between **ACADEMY** and **COLLEGE** to provide **Academic support by conducting Competitive Exam & CRT Training classes** for our Third & Fourth Year students, for possible joint initiatives, With the purpose of supporting the common goals of both managements, enabling collaborative actions to be planned and implemented, and making effective use of the complementary strengths of both organizations.

Academy pledges to support **COLLEGE** in this regard and provide it with advice and resources within its means to ensure successful implementation of conducting Competitive Exam & CRT Coaching Classes in Campus with a stipulated schedule and achievement of its objectives.

In Case **ACADEMY** and **COLLEGE** have differences concerning the implementation of the Competitive Exam & CRT Coaching Classes they undertake to resolve them through dialogue and discussion. In case such differences cannot be resolved either party can terminate this Memorandum of Understanding by giving **FIFTEEN DAYS** notice in writing to the other party and make such termination public only after it has taken effect. This Memorandum of Understanding will remain in effect unless terminated according to the above provision.

GLOBAL INSTITUTE OF ENGINEERING & TECHNOLOGY
Chilkur (V).
Moinabad (M).
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SHARADHA
COACHING ACADEMY

GLOBAL
Institute of Engineering &Technology

Rules and Regulations:

- All the classes of concerned modules are to be conducted only in the college campus by eminent trainers, after college hours.
- The Academy should not demand any amount from the student other than the honorarium provided by the college.
- If any dispute or difference of any kind whatsoever where to arise between INSTITUTION and COLLEGE, authorized Coordinator of the INSTITUTION may Approach only the Principal of COLLEGE.

The implementation of this MOU shall be renewed annually & however the MOU will be in force for **5 years.**

Now, therefore this Memorandum of Understanding is signed on this Date: 19th August 2017, subject to such corporate approvals deemed necessary and shall remain in effect unless terminated in accordance with the above provision.

For and on behalf of

D Sprasad.

Dr. D.S. Prasad
General Manager
Sharadha Coaching Academy

19/8/17

Dr. V.V.V.S. Murthy
Principal
Global Institute of Engineering & Technology

PRINCIPAL
Global Institute of Engg. & Tech
Chilkur (V), Moinabad (M)

SHARADA

INSTITUTE OF ENGINEERING & TECHNOLOGY
Chilkur (V).
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Department of Electronics & Communication Engineering

Time Table for Competitive Exams Training/CRT Classes

A.Y : 2019-20

S.No.	Day	Year	Name of the Training Programme
1	Monday & Tuesday	ECE-III, IV	GATE/PGECET
2	Wednesday & Thursday	ECE-II, III, IV	CRT
3	Friday & Saturday	ECE-III, IV	IELTS, TOEFL, PTE, GRE

Coordinator

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Competitive Exam Training Students of ECE-III & IV Yr
Academic Year : 2019-20

SYLLABUS for TOEFL/IELTS/PTE

Schedule : Friday & Saturday **Time: 4:00 PM-5.00 PM**

MODULE 1: (8 WEEKS)

S.No.	Subjects	Duration (in Weeks)	Name of the Instructor
1	Listening	2	Mr.Vaman Rao,
2	Speaking	2	Mrs.Mangaveni
3	Analytical Writing	2	Mohammed Maqbool Ali
4	Verbal Reasoning	2	Mr.Shailesh Yadav

MODULE 2: (6 WEEKS)

S.No.	Subjects	Duration (in Weeks)	Name of the Instructor
1	Reading	1	Mr.Krupakar Bharani
2	Writing	1	Mr.Sathya Narayana.P
3	Quantitative Reasoning	2	Mrs.Manjula.G
4	AWA	2	Mr.Rama Chandra.P
	Mock Exam		

Co-Ordinator **HEAD**

HEAD
Department of Electronics & Communication Engg.
Global Institute of Engineering & Technology
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Competitive Exam Training Students of ECE-III & IV Yr

Academic Year : 2019-20

TOEFL Syllabus:

- The test has 4 sections:
  Reading measures your ability to understand academic reading material written in English – 60-80 minutes; 36-56 questions

1. **Listening** measures your ability to understand spoken English as it is used in colleges and universities – 60-90 minutes; 34-51 questions
2. **Speaking** measures your ability to speak English in an academic context – 20 minutes; 6 tasks
3. **Writing** measures your ability to write in English in a way that is appropriate for college and university course work – 50 minutes; 2 tasks

- The test is delivered via the Internet at secure, ETS-approved test centers. It emphasizes integrated skills and helps confirm that you are ready to communicate your ideas about what you will read and listen to in English in your academic courses. Integrated tasks require you to combine more than one skill. You'll be asked to:

1. Read, listen, and then speak in response to a question
2. Listen and then speak in response to a question
3. Read, listen, and then write in response to a question

- The test you take may include extra questions in the Reading or Listening section that do not count toward your score. These questions are used either to enable ETS to make scores comparable across test administrations, or they may be new questions being used to help determine how such questions will function under actual testing conditions.
- Each section of the test has a time limit. If you finish a section early, you can go on to the next section, but you cannot go back to a section you have already completed or for which time has been called. However, you can go back within the Reading section during the time for that section. There is a mandatory 10-minute break midway through the testing session.
- You should work quickly but carefully on the Listening and Reading sections. Some questions are more difficult than others, but you should try to answer every one to the best of your ability. If you are not sure of the answer to a question, make the best guess that you can. The questions in the Speaking and Writing sections are each separately timed. Try to answer every one of these questions as completely as possible in the time permitted. For the Speaking and Writing sections, respond only on the assigned topic. If you respond on a different topic, your response will not be scored.

Co-Ordinator **HEAD**

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Department of Electronics And Communication Engineering

Competitive Exam Training Students of ECE-III & IV Yr
Academic Year : 2019-20
SYLLABUS for GATE/PGCET/GRE

Schedule: Monday & Tuesday Time: 4:00 PM - 5.00 PM

Total No.of Courses	14
No.of Modules	4
Courses per Module 1 (III Yr-I Sem)	4
Courses per Module 2 (III Yr-II Sem)	5
Courses per Module 3 (IV Yr-I Sem)	3
Courses per Module 4 (IV Yr-II Sem)	2

Module -I			
S.No	**Course**	**Weeks**	**Faculty**
1	Networks	2	Mr. Kamalakar Reddy,GIET
2	Electronic Devices	2	Mr. Sai Baba, SCA
3	Analog Circuits	2	Mr.Chenna Kesavulu, SCA
4	Digital Circuits	2	Mr.Virender, SCA

Module -II			
S.No	**Course**	**Weeks**	**Faculty**
1	Signals And Systems	2	Mrs.Kiranmay, SCA
2	Aptitude-1	2	Mr.Sai Baba, SCA
3	Control Systems	1	Mr.S Ravi,GIET
4	Analog Communication	1	Dr.P.Markandeya Raju,VGI
5	Digital Communication	1	Mrs.Manasa, SCA

Module -III			
S.No	**Course**	**Weeks**	**Faculty**
1	Electromagnetics Theory	3	Mr.Chaitanya,SCA
2	Electromagnetics	2	Dr.Prathap Reddy, SCA
3	Engineering Mathematics	3	Dr.Chandu Subba Rao, SCA

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Module -IV			
S.No	**Course**	**Weeks**	**Faculty**
1	Aptitude-2	3	Mr.Sai Baba, SCA
2	English Grammar	3	Mr.Satya Narayana,GIET
3	**Mock Gate Exam**		

Co-Ordinator **Head**

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Global Institute of Engineering & Technology
Chilkur (V), Moinabad (M), R.R. Dist. T.S.-501504

ECE	ELECTRONICS AND COMMUNICATION ENGINEERING

Engineering Mathematics

Linear Algebra: Matrix algebra; Systems of linear equations; Eigen values and Eigenvectors.

Calculus: Functions of single variable; Limit, continuity and differentiability; Mean valuetheorems, local maxima and minima, Taylor and Maclaurin series; Evaluation of defini teand indefinite integrals, application of definite integral to obtain area and volume; Partial derivatives; Total derivative; Gradient, Divergence and Curl, Vector identities, Directional derivatives, Line, Surface and Volume integrals, Stokes, Gauss and Green's theorems.

Ordinary Differential Equation (ODE): First order (linear and non- linear) equations; higher order linear equations with constant coefficients; Euler -Cauchy equations; Laplace transform and its application in solving linear ODEs; initial and boundary value problems.

Partial Differential Equation (PDE): Fourier series; separation of variables; solutions of one- dimensional diffusion equation; first and second order one- dimensional wave equation and two-dimensional Laplace equation.

Probability and Statistics: Definitions of probability and sampling theorems; Conditional probability; Discrete Random variables: Poisson and Binomial distributions; Continuous random variables: normal and exponential distributions; Descriptive statistics - Mean, median, mode and standard deviation; Hypothesis testing.

Numerical Methods: Accuracy and precision; error analysis. Numerical solutions of linear and non- linear algebraic equations; Least square approximation, Newton's and Lagrange polynomials, numerical differentiation, Integration by trapezoidal and Simpson's rule, single and multi-step methods for first order differential equations.

Electronics Engineering - ECE

Networks: Network graphs: matrices associated with graphs; incidence, fundamental cut set and fundamental circuit matrices. Solution methods: nodal and mesh analysis. Network theorems: superposition, Thevenin and Norton's maximum power transfer, Wye-Delta transformation. Steady state sinusoidal analysis using phasors. Linear constant coefficient differential equations; time domain analysis of simple RLC circuits, Solution of network equations using Laplace transform: frequency domain analysis of RLC circuits. 2-port network parameters: driving point and transfer functions. State equations for networks.

Electronic Devices: Energy bands in silicon, intrinsic and extrinsic silicon. Carrier transport in silicon: diffusion current, drift current, mobility, and resistivity. Generation and recombination of carriers. p-n

junction diode, Zener diode, tunnel diode, BJT, JFET, MOS capacitor, MOSFET, LED, p-I-n and avalanche photo diode, Basics of LASERs. Device technology: integrated circuits fabrication process, oxidation, diffusion, ion implantation, photolithography, n-tub, p-tub and twin-tub CMOS process.

Analog Circuits: Small Signal Equivalent circuits of diodes, BJTs, MOSFETs and analog CMOS. Simple diode circuits, clipping, clamping, rectifier. Biasing and bias stability of transistor and FET amplifiers. Amplifiers: single-and multi-stage, differential and operational, feedback, and power. Frequency response of amplifiers. Simple op-amp circuits. Filters. Sinusoidal oscillators; criterion for oscillation; single-transistor and op-amp configurations. Function generators and wave-shaping circuits, 555 Timers. Power supplies.

Digital circuits: Boolean algebra, minimization of Boolean functions; logic GATEs; digital IC families (DTL, TTL, ECL, MOS, CMOS). Combinatorial circuits: arithmetic circuits, code converters, multiplexers, decoders, PROMs and PLAs. Sequential circuits: latches and flip-flops, counters and shift-registers. Sample and hold circuits, ADCs, DACs. Semiconductor memories. Microprocessor(8085): architecture, programming, memory and I/O interfacing.

Signals and Systems: Definitions and properties of Laplace transform, continuous-time and discrete-time Fourier series, continuous-time and discrete-time Fourier Transform, DFT and FFT, z-transform. Sampling theorem. Linear Time-Invariant (LTI) Systems: definitions and properties; causality, stability, impulse response, convolution, poles and zeros, parallel and cascade structure, frequency response, group delay, phase delay. Signal transmission through LTI systems.

Control Systems: Basic control system components; block diagrammatic description, reduction of block diagrams. Open loop and closed loop (feedback) systems and stability analysis of these systems. Signal flow graphs and their use in determining transfer functions of systems; transient and steady state analysis of LTI control systems and frequency response. Tools and techniques for LTI control system analysis: root loci, Routh-Hurwitz criterion, Bode and Nyquist plots. Control system compensators: elements of lead and lag compensation, elements of Proportional-Integral-Derivative (PID) control. State variable representation and solution of state equation of LTI control systems.

Communications: Random signals and noise: probability, random variables, probability density function, autocorrelation, power spectral density. Analog communication systems: amplitude and angle modulation and demodulation systems, spectral analysis of these operations, superheterodyne receivers; elements of hardware, realizations of analog communication systems; signal-to-noise ratio (SNR) calculations for amplitude modulation (AM) and frequency modulation (FM) for low noise conditions. Fundamentals of information theory and channel capacity theorem. Digital communication systems: pulse code modulation (PCM), differential pulse code modulation (DPCM), digital modulation schemes: amplitude, phase and

frequency shift keying schemes (ASK, PSK, FSK), matched filter receivers, bandwidth consideration and probability of error calculations for these schemes. Basics of TDMA, FDMA and CDMA and GSM.

Electromagnetics: Elements of vector calculus: divergence and curl; Gauss' and Stokes' theorems, Maxwell's equations: differential and integral forms. Wave equation, Poynting vector. Plane waves: propagation through various media; reflection and refraction; phase and group velocity; skin depth. Transmission lines: characteristic impedance; impedance transformation; Smith chart; impedance matching; S parameters, pulse excitation. Waveguides: modes in rectangular waveguides; boundary conditions; cut-off frequencies; dispersion relations. Basics of propagation in dielectric waveguide and optical fibers. Basics of Antennas: Dipole antennas; radiation pattern; antenna gain.

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Department of Electronics & Communication Engineering

Competitive Exam Training for Students of ECE-III &IV Yr

Academic Year : 2019-20

LIST OF STUDENTS ENROLLED

S.NO.	ROLL NUMBER	NAME OF THE COLLEGE	YEAR
1	15U61A0417	L SRIKANTH REDDY	IV
2	15U61A0427	VEM VAMSHI	IV
3	16U61A0401	AFREEN BEGUM	IV
4	16U61A0405	BADDAMOLLA SHIREESHA	IV
5	16U61A0410	DESIBOYINA BHARGAV SAI	IV
6	16U61A0413	JANGAM VIKAS KUMAR	IV
7	16U61A0414	KAUSER BEGUM	IV
8	16U61A0418	MOHD KAZIM SHARIF	IV
9	16U61A0419	N SOWMYA	IV
10	16U61A0420	NALLA POOJA REDDY	IV
11	16U61A0421	NANDAMURI NARENDRA	IV
12	16U61A0424	SHEELAM RAMAKRISHNA	IV
13	16U61A0425	SUMMAIYA	IV
14	16U61A0426	YANAGANDULA SURESH	IV
15	17U65A0401	A YAMUNA	IV
16	17U65A0402	KARUPURAM BHAVANA	IV
17	17U65A0403	MOHAMMED ABDUL BARI KHAN	IV
18	17U65A0405	SANKOJI KRISHNAVENI	IV
19	16U61A0403	ASRA FARDEEN	III
20	16U61A0407	BOGULAPALLY KARTHIK KUMAR REDDY	III
21	17U61A0401	ARACHI JOHNSON	III
22	17U61A0403	DEVISETTY BALAJI	III
23	17U61A0404	GADDAM RAKESH	III
24	17U61A0405	HAREESH REDDY	III
25	17U61A0406	JANA SANDHYA	III
26	17U61A0409	KAKARLA MRUDULA	III
27	17U61A0410	KANCHARWAR RAHUL REDDY	III
28	17U61A0411	KASARI UDAY SAGAR	III
29	17U61A0412	LACHIREDDY BHAGAVATHI	III
30	17U61A0418	PAKALA MANOJ REDDY	III
31	17U61A0420	RACHA SRUTHI	III
32	17U61A0421	SOMIREDDY BHARGAVI	III
33	17U61A0423	T. DIVYA SRI DURGA	III

Co-Ordinator
Ms.Imrose Basha Basha
Assistant Professor
Department of ECE

Head

HEAD
Department of Electronics & Communication Engg.
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Competitive Exam Training/CRT for Students of ECE-III &IV Yr
Academic Year : 2019-20

S.NO	ROLL NUMBER	NAME OF THE STUDENT	YEAR	1	2	3	4	5	6	7	8	9	10	Total
1	15U61A0417	L SRIKANTH REDDY	IV	1	2	3	0	4	5	6	7	8	9	9
2	15U61A0427	VEM VAMSHI	IV	1	2	3	4	5	6	7	8	9	10	10
3	16U61A0401	AFREEN BEGUM	IV	1	2	3	4	5	6	7	8	9	10	10
4	16U61A0405	BADDAMOLLA SHIREESHA	IV	1	2	3	0	4	5	6	7	8	9	9
5	16U61A0410	DESIBOYINA BHARGAV SAI	IV	1	2	3	4	0	5	6	7	8	9	10
6	16U61A0413	JANGAM VIKAS KUMAR	IV	1	2	3	4	5	6	7	0	8	9	9
7	16U61A0414	KAUSER BEGUM	IV	1	2	3	4	5	6	7	8	9	10	10
8	16U61A0418	MOHD KAZIM SHARIF	IV	1	2	3	4	5	6	7	8	9	10	10
9	16U61A0419	N SOWMYA	IV	1	2	3	4	5	6	7	8	9	10	10
10	16U61A0420	NALLA POOJA REDDY	IV	1	2	3	0	4	5	6	7	8	9	9
11	16U61A0421	NANDAMURI NARENDRA	IV	1	2	3	4	5	6	7	8	9	10	10
12	16U61A0424	SHEELAM RAMAKRISHNA	IV	1	2	3	4	5	6	7	8	9	10	10
13	16U61A0425	SUMMAIYA	IV	1	2	3	4	5	6	7	8	9	10	10
14	16U61A0426	YANAGANDULA SURESH	IV	1	2	3	4	5	6	7	8	9	10	10
15	17U65A0401	A YAMUNA	IV	1	2	3	0	4	5	6	7	8	9	9
16	17U65A0402	KARUPURAM BHAVANA	IV	1	2	3	4	5	6	7	8	9	10	10
17	17U65A0403	MOHAMMED ABDUL BARI KHAN	IV	1	2	3	4	5	6	7	8	9	10	10
18	17U65A0405	SANKOJI KRISHNAVENI	IV	1	2	3	0	4	5	6	7	8	9	9
19	16U61A0403	ASRA FARDEEN	III	1	2	3	4	5	6	7	8	9	10	10

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Department of Electronics and Communication Engineering

20	16U61A0407	BOGULAPALLY KARTHIK KUMAR REDDY	III	1	2	3	0	4	5	6	7	0	8	8
21	17U61A0401	ARACHI JOHNSON	III	1	2	3	4	5	6	7	8	0	9	9
22	17U61A0403	DEVISETTY BALAJI	III	1	2	0	3	4	5	6	7	8	9	9
23	17U61A0404	GADDAM RAKESH	III	1	2	3	4	5	6	7	8	9	10	10
24	17U61A0405	HAREESH REDDY	III	1	2	3	4	5	6	7	8	9	10	10
25	17U61A0406	JANA SANDHYA	III	1	0	2	3	4	5	6	7	8	9	9
26	17U61A0409	KAKARLA MRUDULA	III	1	2	3	4	5	6	7	8	9	10	10
27	17U61A0410	KANCHARWAR RAHUL REDDY	III	1	2	0	3	4	5	6	7	8	9	9
28	17U61A0411	KASARI UDAY SAGAR	III	1	2	3	4	5	6	7	8	9	10	10
29	17U61A0412	LACHIREDDY BHAGAVATHI	III	1	2	3	4	5	6	7	8	9	10	10
30	17U61A0418	PAKALA MANOJ REDDY	III	1	2	3	4	5	6	7	8	9	10	10
31	17U61A0420	RACHA SRUTHI	III	1	2	0	3	4	5	6	7	8	9	9
32	17U61A0421	SOMIREDDY BHARGAVI	III	1	2	3	4	5	6	7	8	9	10	10
33	17U61A0423	T. DIVYA SRI DURGA	III	1	2	3	4	5	6	7	8	9	10	10

Co-Ordinator

Head

HEAD
Department of Electronics & Communication Engg.
Global Institute of Engineering & Technology
Chilkur (V), Moinabad (M), R.R. Dist. T.S.-501504

GLOBAL
Institute of Engineering & Technology
(Approved by AICTE & Affiliated to JNTUH)
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JNTUH Code (U6) CIVIL-CSE-MECH-ECE-EEE-MBA-M.Tech. EAMCET Code – GLOB

Department Of Electronics And Communication Engineering

Competitive Exam Training for Students of ECE II&IV Yr
Academic Year : 2019-20

FEEDBACK FORM

Please valuate your rating of the course by placing a tick in the appropriate box.
1. Poor 2. Satisfactory 3. Good 4. Very good 5. Excellent

Date: 29/02/2020

ASPECTS	RATING				
	Excellent 5	Very good 4	Good 3	Satisfactory 2	Poor 1
Coverage of important topics		✓			
Trainer was knowledgeable and skillful	✓				
Quality of input provided	✓				
Presentation in class		✓			
Adherence to the time schedule	✓				
Opportunity given to clear doubts	✓				
Overall learning experience	✓				

How did the training classes enhance your skills?

Very Good

Specify problems faced by you during the training classes?

—

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Department of Electronics And Communication Engineering

A Report of
Guidance on Competitive Exams
Academic Year : 2019-20

As a quality Initiative, Department of Electronics And Communication Engineering in association with Sharada Coaching Academy has duly conducted Competitive Exam Training Classes for student UG-ECE-III, and IV years for Academic Year 2019-20 from 23-09-2019 to 29-02-2020 from 4.00 PM to 5.00 PM.

A circular was given to class mentor before 1st Midterm Examination to prepare list of Fast & Slow Learners, based on student's performance in Formative Assessments, Midterm Results and in consultation with respective Course Faculty. A List of Fast & Slow learners is submitted by respective Class Mentors to the Head, through Head Mentor.

A Week after Commencement of 1st Midterm Examinations, students were given a circular regarding conduct of Competitive Exam Training Classes.

Modules were designed for Various competitive exams like GATE, IES, TOEFL, GRE , PGCET, IELTS, PTE. Time tables for these classes were made such that training for National

Level Competitive Examinations were given on Mondays & Tuesdays and training for International Level Competitive Examinations for aspirants were given on Fridays & Saturdays, by eminent faculty of GIET in association with Sharada Coaching Academy.

Total 33 Students, of which 16 students from III Year, 17 students from ECE-IV Year benefited through Competitive Exams Training classes.

Guidance for Competitive Examinations

Day to day monitoring & conduct of classes was monitored by the coordinator of Competitive Examinations & Attendance was taken at the end of classes scheduled from 4 PM to 5 PM. important topics were covered from course. Students were also made to practice previous papers of Competitive Examinations.

At the end of training, a circular was sent to for writing a Mock Examination. Mock Examinations were conducted and results of the same were announced. Feedbacks were collected from students at the end of the training.

This initiative has proved to be a Booster in performance of Fast Learners in Various National & International Level Competitive Examinations.

COORDINATOR

GLOBAL INSTITUTE OF ENGINEERING & TECHNOLOGY
Chilkur (V), Moinabad (M), R.R. Dist. (T.S.) GC-U6

HEAD

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