



GLOBAL Institute of Engineering & Technology

(Approved by AICTE & Affiliated to JNTUH)

Survey No. 179, Chilkur (V), Moinabad (M), Ranga Reddy Dist. TS.

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JNTUH Code(U6)

CIVIL – CSE – MECH – ECE – EEE – MBA – M.Tech.

EAMCET Code-GLOB

Department of Electronics and Communication Engineering

G Ahmed Zeeshan

BE.,M.Tech.,(Ph.D)

MISTE.,MISOL.,MIEEEE(USA)

Assistant Professor & Head

Date: 24/12/2018

CIRCULAR

The Department of Electronics and Communication Engineering is hosting for *A Two Day Workshop on PCB Design & Fabrication* for the students of B. Tech.(ECE) in Room No. 04 on 08th January 2019 & 09th January 2019. All the students of B.Tech (ECE) are hereby informed to register for the course on or before 07th January 2019. The Co-Ordinator for the course is Ms. M. Sai Chandana, Assistant Professor, Department of ECE.

Zeeshan
24/12/2018
HEAD

Department of Electronics & Communication Eng
Global Institute of Engineering & Technolog
Chilkur (V), Moinabad (M), R.R. Dist.T.S.-50150

Cc to:
Principal
Co ordinator IQAC
Mentors
Training & Placement Cell
Notice board
File



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SURVEY NO 179, CHILKUR(V), MOINABAD(M), RANGAREDDY DIST, T.S

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
A Two Day Workshop
On
PCB Design & Fabrication
(08th & 09th January, 2019)
PROGRAMME SCHEDULE

	TIME	SESSION	SPEAKER
Day 1	9:30 AM - 10:00 AM	Inauguration	
	10:00 AM - 11:00 AM	Description of Electronic Component	Mrs. Nuzath Unnisa Begum, Asst. Professor ECE, GIET
	11:00 AM - 11:30 AM	HIGH TEA	
	11:30 AM - 01:00 PM	Introduction to PCB Layout	Mr. Ahmed Zeeshan, Asst. Professor & Head ECE, GIET
	01:00 PM - 02:00 PM	LUNCH BREAK	
	02:00 PM - 03:30 PM	Technologies, Materials, Software tools in PCB Design	Mr. Indranil Gupta, Technical Engineer, Coreel Technologies Pvt. Ltd, Hyderabad
Day 2			
	09:30 AM - 11:00 AM	PCB Fabrication Process	Dr. Ravi Kumar, Professor of ECE, Siddhartha Inst. of Engg. & Tech.
	11:00 AM - 11:15 AM	TEA BREAK	
	11:15 AM - 01:00 PM	Testing and Trouble shootings	Mr. Indranil Gupta, Technical Engineer, Coreel Technologies Pvt. Ltd, Hyderabad
	01:00 PM - 02:00 PM	LUNCH BREAK	
	02:00 PM - 03:30 PM	Valedictory Function, Feedback and Certificate Distribution	

Ms. M. Sai Chandana
Co-Ordinator
Ms. M. Sai Chandana
Assistant Professor
Department of ECE



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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

A Two Day Workshop

On

PCB Design & Fabrication
(08th & 09th January, 2019)
REGISTRATION FORM
(Please Fill in CAPITAL LETTERS)

Name : AKASH JOHNSON

Roll No : 17U61A0401

Course/Year : B. TECH / II

Department : ECE

E-mail : akashjohnson611999@gmail.com

Phone No : 9505167139

Signature of Participant
akash

For Office use only

Signature of Coordinator
akash

Approved/ Not Approved



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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
A Two Day Workshop

On
PCB DESIGN & FABRICATION

(08th & 09th January, 2019)

LIST OF PARTICIPANTS

S. NO:	NAME OF THE PARTICIPANT
1.	MD ABDUR RAHMAN
2.	ARACHI JOHNSON
3.	DEVISSETTY BALAJI
4.	GADDAM RAKESH
5.	HAREESH REDDY
6.	JANA SANDHYA
7.	JANGA THARUN REDDY
8.	JANGATWAR TAGORE PRADEEP KUMAR
9.	KAKARLA MRUDULA
10.	KANCHARWAR RAHUL REDDY
11.	KASARI UDAY SAGAR
12.	LACHIREDDY BHAGAVATHI
13.	MEKALA NANDU
14.	MOHAMMED ASFIYAN AYUB
15.	PAKALA MANOJ REDDY
16.	RACHA SRUTHI
17.	SOMIREDDY BHARGAVI
18.	SUNKARI SUDHEER
19.	T. DIVYA SRI DURGA
20.	THATIPALLY ANWESH
21.	THATIPALLY JEEVANAJYOTHI
22.	TUMMALAPALLI N S LALITHA SREE ALEKHYA
23.	K DURGABHAVANI

Beeth
08/01/2019
Co- Ordinator
Ms. M. Sai Chandana
Assistant Professor
Department of ECE



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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING A Two Day Workshop

On
PCB Design & Fabrication
(08th & 09th January, 2019)
ATTENDANCE SHEET

S.No.	Name of the Participant	Programme	Year	Signature			
				Day-1 (08/01/2019)	AN	FN	AN
1.	MD ABDUR RAHMAN	B.Tech	II	<i>MD</i>	<i>MD</i>	<i>MD</i>	<i>MD</i>
2.	ARACHI JOHNSON	B.Tech	II	<i>Arachi</i>	<i>Arachi</i>	<i>Arachi</i>	<i>Arachi</i>
3.	DEVISSETTY BALAJI	B.Tech	II	<i>Balaji</i>	<i>Balaji</i>	<i>Balaji</i>	<i>Balaji</i>
4.	GADDAM RAKESH	B.Tech	II	<i>G Rakesh</i>	<i>G Rakesh</i>	<i>G Rakesh</i>	<i>G Rakesh</i>
5.	HAREESH REDDY	B.Tech	II	<i>Hareesh</i>	<i>Hareesh</i>	<i>Hareesh</i>	<i>Hareesh</i>

Co Ordinator
Prakash

Head of the Department
Institute of Electronics & Communication Engg.
Global Institute of Engineering & Technology
Chilukur (V), Moinebad (M), R.R. Dist. T.S.-501569



Principal
Global Institute of Engineering & Technology
Chilukur (V), Moinebad (M)
R. R. Dist

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(08th & 09th January, 2019)
ATTENDANCE SHEET

S.No.	Name of the Participant	Programme	Year	Signature			
				Day-1 (08/01/2019)	AN	FN	AN
11.	KASARI UDAY SAGAR	B.Tech	II	<i>K. Sagar</i>	<i>K. Sagar</i>	<i>K. Sagar</i>	<i>K. Sagar</i>
12.	LACHIREDDY BHAGAVATHI	B.Tech	II	<i>L. Bhavi</i>	<i>L. Bhavi</i>	<i>L. Bhavi</i>	<i>L. Bhavi</i>
13.	MEKALA NANDU	B.Tech	II	<i>Nandu</i>	<i>Nandu</i>	<i>Nandu</i>	<i>Nandu</i>
14.	MOHAMMED ASFIYAN AYUB	B.Tech	II	<i>Asfiyan</i>	<i>Asfiyan</i>	<i>Asfiyan</i>	<i>Asfiyan</i>
15.	PAKALA MANOJ REDDY	B.Tech	II	<i>Manoj</i>	<i>Manoj</i>	<i>Manoj</i>	<i>Manoj</i>

Co Ordinator *P. S. Srinivas*

Department of Head of the Department
Global Institute of Engineering & Technology
Chilkur (V), Moynabad (M), R. R. Dist. T.S.-50150



Principal *P. S. Srinivas*
Global Institute of Engg. & T.
Chilkur (V), Moynabad (M)
R. R. Dist. T.S.



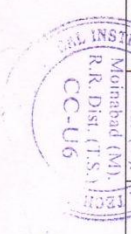
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S.No.	Name of the Participant	Programme	Year	Signature			
				Day-1 (08/01/2019)		Day-2 (09/01/2019)	
				FN	AN	FN	AN
6.	JANA SANDHYA	B.Tech	II	<i>Jandya</i>	<i>Jandya</i>	<i>Jandya</i>	<i>Jandya</i>
7.	JANGA THARUN REDDY	B.Tech	II	<i>S.Reddy</i>	<i>S.Reddy</i>	<i>S.Reddy</i>	<i>S.Reddy</i>
8.	JANGATWAR TAGORE PRADEEP KUMAR	B.Tech	II	<i>P.Kumar</i>	<i>P.Kumar</i>	<i>P.Kumar</i>	<i>P.Kumar</i>
9.	KAKARLA MRUDULA	B.Tech	II	<i>K.Mrudula</i>	<i>V.V.Reddy</i>	<i>K.Mrudula</i>	<i>V.V.Reddy</i>
10.	KANCHARWAR RAHUL REDDY	B.Tech	II	<i>R.Reddy</i>	<i>R.Reddy</i>	<i>R.Reddy</i>	<i>R.Reddy</i>

Co Ordinator *R.R. Dist*
 Head of the Department *T. Tech*
 Department of Electronics and Communication Engineering
 Global Institute of Engineering & Technology
 Chilkur (V), Moinabad (M), R.R. Dist. T.S-50156

Principal *R.R. Dist*
 Global Institute of Engg. & Techn
 Chilkur (V), Moinabad (M)
 R. R. Dist





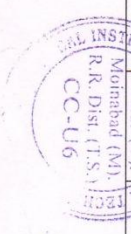
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S.No.	Name of the Participant	Programme	Year	Signature			
				Day-1 (08/01/2019)		Day-2 (09/01/2019)	
				FN	AN	FN	AN
6.	JANA SANDHYA	B.Tech	II	<i>Jandya</i>	<i>Jandya</i>	<i>Jandya</i>	<i>Jandya</i>
7.	JANGA THARUN REDDY	B.Tech	II	<i>J Reddy</i>	<i>J.Reddy</i>	<i>S.Reddy</i>	<i>S.Reddy</i>
8.	JANGATWAR TAGORE PRADEEP KUMAR	B.Tech	II	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>
9.	KAKARLA MRUDULA	B.Tech	II	<i>K.Mrudula</i>	<i>V.V.Rreddy</i>	<i>K.Mrudula</i>	<i>V.V.Rreddy</i>
10.	KANCHARWAR RAHUL REDDY	B.Tech	II	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>

Co Ordinator *[Signature]*
 Head of the Department *[Signature]*
 Global Institute of Engineering & Technology
 Chilkur (V), Moinabad (M), R.R. Dist.T.S-50156

Principal *[Signature]*
 Global Institute of Engg. & Techn
 Chilkur (V), Moinabad (M)
 R. R. Dist





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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING On A Two Day Workshop

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ATTENDANCE SHEET

S.No.	Name of the Participant	Programme	Year	Signature			
				Day-1 (08/01/2019)	Day-2 (09/01/2019)	FN	AN
21.	THATIPALLY JEEVANAYOTHI	B.Tech	II			FN	AN
22.	TUMMALAPALLI N S LALITHA SREE ALEKHYA	B.Tech	II			FN	AN
23.	K DURGABHAVANI	B.Tech	II			FN	AN

Co Ordinator

Head of the Department

Department of Electronics & Communication Enggs.
Global Institute of Engineering & Technology
Chilkur (V), Moinebad (M), R.R. Dist. T.S-501504



Principal

PRINCIPAL
Global Institute of Engg. & Tech
Chilkur (V), Moinebad (M)
R. R. Dist



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FEEDBACK FORM

Please evaluate your rating of the course by placing a tick in the appropriate box.
1. Poor 2. Satisfactory 3. Good 4. Very good 5. Excellent

Branch and Year: B. Tech IInd Year

Date: 09/01/19

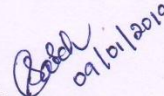
ASPECTS	RATING				
	Excellent 5	Very good 4	Good 3	Satisfactory 2	Poor 1
Relevance of contents	✓				
Trainer was knowledgeable and skillful		✓			
Quality of input provided	✓				
Quality of presentations		✓			
Adherence to the time schedule	✓				
Opportunity given to participant to clear doubts	✓				
Identify ways to build on current skills and knowledge	✓				
Overall learning experience	✓				
How has the course enhanced your skills or understanding of this topic?					
-					
Specify problems faced by you during the course?					
-					

**REPORT
ON
TWO DAY WORKSHOP:
PCB DESIGN & FABRICATION**

A Two day Workshop titled “PCB Design & Fabrication” was organized by department of Electronics & Communication Engineering OF GIET, HYDERABAD the workshop commenced with inaugural ceremony at 9:30 AM Ms. Sai Chandana, Assistant Professor ECE, GIET. Welcomed all the dignitaries present on the dais and colleagues & students. He mentioned about the significance of the workshop. Mr. Indranil Gupta, Technical Engineer, Coreel Technologies Pvt. Ltd. Hyderabad described the PCB and how it is connected to different fields with example. He motivated everyone to actively participate in the workshop to get benefited.

Day 1: The Technical session started at 10:00 AM, Mrs. Nuzath Unnisa Begum, Assistant Professor ECE, GIET. Delivered a topic on “Description of Electronic Component”. Mam discussed about description of Electronic Component. Mr. Ahmed Zeeshan, Assistant Professor & Head, GIET explained the “Introduction to PCB layout” in which sir explained about PCB layout like substrate parameters, thickness etc. from 11:30 AM to 1:00 PM. The next speaker Mr. Indranil Gupta, Technical Engineer, Coreel Technologies Pvt.Ltd., Hyd lectured on “Technologies, Materials, Software tools in PCB Design” which includes different technologies used for designing of PCB. He also discussed the types of substrates used and dielectric constants and various tools from 2:00 PM to 3:30 PM.

Day 2: Session started at 9:30 am, Dr. Ravi Kumar, Professor of ECE, Siddhartha Inst. of Engg. & Tech. delivered a lecture on “PCB Fabrication process”. He discussed about PCB Fabrications steps and explained each step with its schematic diagram in his session. Followed by second lecture on “Testing and Trouble shooting” by Mr. Indranil Gupta, Technical Engineer, Coreel Technologies Pvt.Ltd, Hyderabad Both the sessions gives hands on experience of PCB Fabrication process. The workshop concludes with validatoratory function, feedback and certificate distribution.


Co Ordinator
Ms. M.Sai Chandana
Assistant Professor
Department of ECE

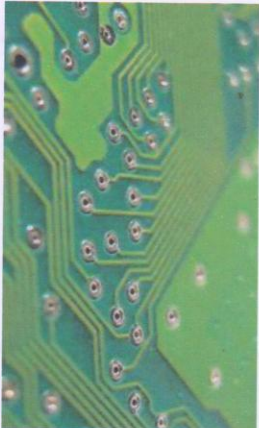


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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING



A Two Day Workshop
On
PCB DESIGN & FABRICATION
(08th & 09th January 2019)

CERTIFICATE OF PARTICIPATION

This is to certify that Ms./Mr. RACHA SRUTHI

bearing roll number 17UG1A0420 has attended a Two Day Workshop on "PCB DESIGN & FABRICATION"

held on 08th January 2019 & 09th January 2019 at Global Institute of Engineering and Technology.

Rach
Co-Ordinator
(M. Sai Chandana)

Zesh
Head of the Department
(G. Ahmed Zeshan)

K Sri
Principal
(Dr. K. Sivalingam)



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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING




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PCB DESIGN & FABRICATION
(08th & 09th January 2019)

CERTIFICATE OF PARTICIPATION

This is to certify that Ms./Mr. ARACHT JOHNSON

bearing roll number 17UG1A0401 has attended a Two Day Workshop on "PCB DESIGN & FABRICATION"

held on 08th January 2019 & 09th January 2019 at Global Institute of Engineering and Technology.

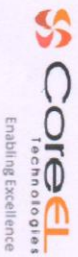

Co-Ordinator
(M. Sai Chandana)


Head of the Department
(G. Ahmed Zeeshan)


Principal
(Dr. K. Sivalingam)



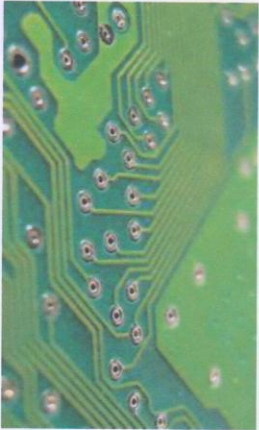
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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING



A Two Day Workshop

On

PCB DESIGN & FABRICATION

(08th & 09th January 2019)

CERTIFICATE OF PARTICIPATION

This is to certify that Ms./Mr. _____

Gaddam Raksha
GADDAM RAKSHA

bearing roll number 17UG1A0404 has attended a Two Day Workshop on "PCB DESIGN & FABRICATION"

held on 08th January 2019 & 09th January 2019 at Global Institute of Engineering and Technology.

Sai
Co-Ordinator
(M. Sai Chandana)

Zeeshan
Head of the Department
(G. Ahmed Zeeshan)

K. S. Sivalingam
Principal
(Dr. K. Sivalingam)