

ABOUT INSTITUTION:

Global Institute of Engineering and Technology, Hyderabad, Telangana is affiliated to JNTUH & approved by AICTE. GIET was established in the year 2006 with a vision. To produce technologically skilled Engineers of world class competency to address global challenges .GIET nurtures and supports a unique system of education structure on values and combines the tenets of the academic excellence with corporate professionalism. The primary objective at Global Institute of Engineering and Technology is to impart quality education which leads to quality living. Located at Moinabad, the sprawling campus spread over 28 acres of lush greenery creates the most conducive environment for education. GIET offers under graduate programmes in ECE ,EEE ,CSE, CE, ME ,CSE(AIML), CSE(DS)* and post graduate programmes Embedded Systems & Structural Engineering.

The system Of educational Global Institute of Engineering & Technology grooms the students to rise into professionals and leaders of high academic caliber with unblemished character. Dedicated ,experienced and qualified faculty help in honing the skills of students at early level of their learning.

ABOUT THE ECE DEPARTMENT:

The Department has good infrastructural facilities and is equipped with full-fledged laboratories. The Department also has audio-visual teaching aids with LCD for effective teaching. Keeping pace with the Global Trends GIET has established advanced labs in Digital Electronics, Mobile Communication ,Embedded Systems ,VLSI Design, Microwave Engineering etc. Main thrust of ECE Department is to provide better quality education required by the present industry and train the students in new technologies to equip them to cater to the present global requirements. The Department is offering M.Tech in Embedded Systems.

*Waiting for approval from JNTU

ABOUT THE CERTIFICATE COURSE:

This course elucidates concepts related to PCB fabrication is the process or procedure that transforms a circuit board design into a physical structure based upon the specifications provided in the design package. This physical manifestation is achieved through the following actions or techniques: Imaging desired layout on copper clad laminates.

Speakers:

- Mrs.Nuzath Unnisa Begum
Asst. Professor,ECE ,GIET
- Mr. Ahmed
Zeeshan Asst.
Professor and Head
ECE ,GIET.
- Mr. Indranil Gupta,
Technical Engineer,Coreel
Technologies Pvt.Ltd,Hyd.

TOPICS TO BE COVERED:

- Introduction to PCB layout
- Description of Electronic Component
- Technologies, Materials, Software tools in PCB Design
- PCB fabrication process
- Testing and Trouble shooting

Below are the means to help you with your self-reflection, self-verification:

1. **Attendance:** This Certificate Course is like a sentence spoken over 6 days. With Hands on. Participation in all sessions is desirable to get all the proposals being put forth.
2. **Assignments:** There will be session wise assignment which need to be submitted.
3. **Course Outcome Evaluation:**
Exam will be Conducted on last of Course for which you need to get 80% in order to receive Certificate.
4. **Feedback**

ORGANIZING COMMITTEE:

Chief Patron:

Mr. Sabiha Arif uddin
M.Phil
Secretary MEWS

Patron:

1. Mr. KM Fasih uddin,
Director GPC

Co-Patrons:

1. Prof. Ravindra Tiwari,
Dean GPC

2. Prof. K Sivalingam,
Principal GIET

Organizing Secretary:

Mr. G. Ahmed Zeeshan
BE., M.Tech. (Ph.D.), LMISTE
Head ECE

Committee Members:

- | | |
|-----------------------------|--------------------------|
| 1. Dr. S. Padam Priyal | Prof., Dept of ECE |
| 2. Dr. Ch. Venkateswara Rao | Prof., Dept of ECE |
| 3. Mrs. Nuzath Unnisa Begum | Asst. Prof., Dept of ECE |
| 4. Mr. D Kamalakara Reddy | Asst. Prof., Dept of ECE |
| 5. Mr. Faizur Rahman Babul | Asst. Prof., Dept of ECE |
| 6. Ms. Imrose Basha | Asst. Prof., Dept of ECE |
| 7. Mrs. Anwar Jahan | Asst. Prof., Dept of ECE |
| 8. Ms. M Sai Chandana | Asst. Prof., Dept of ECE |
| 9. Mr. G Uday Kumar | Asst. Prof., Dept of ECE |
| 10. Mr. G Hari Krishna | Asst. Prof., Dept of ECE |

How to Apply:

Interested students are required to fill Registration Form online only through Google forms at the link given below

<https://forms.gle/1nkUgovXxDSPA GH89>

Registration should be done between
5/01/2019 to 7/01/2019

Limited Registrations. First Come First
Serve

WHO SHOULD ATTEND?

The Course is designed for B.Tech students of
ECE

COORDINATOR:

Ms. M. Sai Chandana
Asst. Prof Dept of ECE
Mobile: 9704890527

GLOBAL INSTITUTE OF ENGINEERING & TECHNOLOGY



A Workshop On

"PCB Design and Fabrication"
(08th-09th January, 2019)



Organized by
DEPARTMENT OF ELECTRONICS &
COMMUNICATION ENGINEERING
GLOBAL INSTITUTE OF ENGINEERING
& TECHNOLOGY

(A NAAC Accredited Institution, Approved by
AICTE & Affiliated to JNTUH)

Recognized under section 2(f) of UGC Act 1956

An ISO 9001:2015 Certified Institution

Chilukur (V), Moinabad (M), RR Dist - TS

Phone No: +91-9985-359-601

+91-879-010-1015 +91-919959250205

E-mail:

principal.giet.u6@gmail.com gieteng@gmail.com

Website: www.globalhyd.edu.in





GLOBAL Institute of Engineering & Technology

(Approved by AICTE & Affiliated to JNTUH)

Survey No. 179, Chilkur (V), Moinabad (M), Ranga Reddy Dist. TS.

Phone: 8790101015/9959250205

e-mail: principal.giet.u6@gmail.com

JNTUH Code(U6)

CIVIL – CSE – MECH – ECE – EEE – MBA – M.Tech.

EAMCET Code-GLOB

Department of Electronics and Communication Engineering

G Ahmed Zeeshan

BE.,M.Tech.,(Ph.D)

MISTE.,MISOL.,MIEEE(USA)

Assistant Professor & Head

Date: 24/12/2018

CIRCULAR

The Department of Electronics and Communication Engineering is hosting for *A Two Day Workshop on PCB Design & Fabrication* for the students of B. Tech.(ECE) in Room No. 04 on 08th January 2019 & 09th January 2019. All the students of B.Tech (ECE) are hereby informed to register for the course on or before 07th January 2019. The Co-Ordinator for the course is Ms. M. Sai Chandana, Assistant Professor, Department of ECE.

Zeeshan
24/12/2018
HEAD

Department of Electronics & Communication Eng
Global Institute of Engineering & Technolog
Chilkur (V), Moinabad (M), R.R. Dist.T.S.-50150

Cc to:
Principal
Co ordinator IQAC
Mentors
Training & Placement Cell
Notice board
File



GLOBAL Institute of Engineering & Technology

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SURVEY NO 179, CHILKUR(V), MOINABAD(M), RANGAREDDY DIST, T.S

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
A Two Day Workshop
On
PCB Design & Fabrication
(08th & 09th January, 2019)
PROGRAMME SCHEDULE

	TIME	SESSION	SPEAKER
Day 1	9:30 AM - 10:00 AM	Inauguration	
	10:00 AM - 11:00 AM	Description of Electronic Component	Mrs. Nuzath Unnisa Begum, Asst. Professor ECE, GIET
	11:00 AM - 11:30 AM	HIGH TEA	
	11:30 AM - 01:00 PM	Introduction to PCB Layout	Mr. Ahmed Zeeshan, Asst. Professor & Head ECE, GIET
	01:00 PM - 02:00 PM	LUNCH BREAK	
	02:00 PM - 03:30 PM	Technologies, Materials, Software tools in PCB Design	Mr. Indranil Gupta, Technical Engineer, Coreel Technologies Pvt. Ltd, Hyderabad
Day 2	09:30 AM - 11:00 AM	PCB Fabrication Process	Dr. Ravi Kumar, Professor of ECE, Siddhartha Inst. of Engg. & Tech.
	11:00 AM - 11:15 AM	TEA BREAK	
	11:15 AM - 01:00 PM	Testing and Trouble shootings	Mr. Indranil Gupta, Technical Engineer, Coreel Technologies Pvt. Ltd, Hyderabad
	01:00 PM - 02:00 PM	LUNCH BREAK	
	02:00 PM - 03:30 PM	Valedictory Function , Feedback and Certificate Distribution	

Chandana
Co- Ordinator
Ms.M. Sai Chandana
Assistant Professor
Department of ECE



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SURVEY NO 179, CHILUKURU, MOINABAD(M), RANGAREDDY DIST.T.S

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

A Two Day Workshop

On

PCB Design & Fabrication
(08th & 09th January, 2019)

REGISTRATION FORM

(Please Fill in CAPITAL LETTERS)

Name : ARACHI JOHNSON

Roll No : 17061A0401

Course/Year : B. TECH / II

Department : ECCE

E-mail : johnjohnson611999@gmail.com

Phone No : 9505167139

Signature of Participant

For Office use only

Approved/ Not Approved

Signature of Coordinator



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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
A Two Day Workshop

On
PCB DESIGN & FABRICATION

(08th & 09th January, 2019)

LIST OF PARTICIPANTS

S. NO:	NAME OF THE PARTICIPANT
1.	MD ABDUR RAHMAN
2.	ARACHI JOHNSON
3.	DEVISSETTY BALAJI
4.	GADDAM RAKESH
5.	HAREESH REDDY
6.	JANA SANDHYA
7.	JANGA THARUN REDDY
8.	JANGATWAR TAGORE PRADEEP KUMAR
9.	KAKARLA MRUDULA
10.	KANCHARWAR RAHUL REDDY
11.	KASARI UDAY SAGAR
12.	LACHIREDDY BHAGAVATHI
13.	MEKALA NANDU
14.	MOHAMMED ASFIYAN AYUB
15.	PAKALA MANOJ REDDY
16.	RACHA SRUTHI
17.	SOMIREDDY BHARGAVI
18.	SUNKARI SUDHEER
19.	T. DIVYA SRI DURGA
20.	THATIPALLY ANWESH
21.	THATIPALLY JEEVANAJYOTHI
22.	TUMMALAPALLI N S LALITHA SREE ALEKHYA
23.	K DURGABHAVANI

Beeth
08/01/2019
Co- Ordinator
Ms. M. Sai Chandana
Assistant Professor
Department of ECE



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SURVEY NO 179, CHILKUR(V), MOINABAD(M), RANGAREDDY DIST, T.S

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
On
A Two Day Workshop
PCB Design & Fabrication
(08th & 09th January, 2019)
ATTENDANCE SHEET

S.No.	Name of the Participant	Programme	Year	Signature			
				Day-1 (08/01/2019)	Day-2 (09/01/2019)		
1.	MD ABDUR RAHMAN	B.Tech	II	FN	AN	FN	AN
2.	ARACHI JOHNSON	B.Tech	II	FN	AN	FN	AN
3.	DEVISSETTY BALAJI	B.Tech	II	FN	AN	FN	AN
4.	GADDAM RAKESH	B.Tech	II	FN	AN	FN	AN
5.	HAREESH REDDY	B.Tech	II	FN	AN	FN	AN

Co Ordinator

Head of the Department

Global Institute of Engineering & Technology
Chilukur (V), Moinebad (M), R.R. Dist, T.S-501504



Principal
Global Institute of Engineering & Technology
Chilukur (V), Moinebad (M)
R. R. Dist

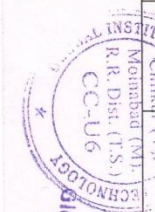
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SURVEY NO 179, CHILUKUR(V), MOINABAD(M), RANGAREDDY DIST.T.S

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
On
PCB Design & Fabrication
(08th & 09th January, 2019)
ATTENDANCE SHEET

S.No.	Name of the Participant	Programme	Year	Signature			
				Day-1 (08/01/2019)		Day-2 (09/01/2019)	
11.	KASARI UDAY SAGAR	B.Tech	II	FN	AN	FN	AN
12.	LACHIREDDY BHAGAVATHI	B.Tech	II	K. Bhof	K. Bhof	K. Bhof	K. Bhof
13.	MEKALA NANDU	B.Tech	II	L. Bhof	L. Bhof	L. Bhof	L. Bhof
14.	MOHAMMED ASFIYAN AYUB	B.Tech	II	Not	Not	Not	Not
15.	PAKALA MANOJ REDDY	B.Tech	II	Not	Not	Not	Not

Co Ordinator

Department of Head of the Department
Global Institute of Engineering & Technology
Chilukur (V), Moinebad (M), R.R. Dist.T.S.-50150



Principal
Global Institute of Engg. & T,
Chilukur (V), Moinebad (M)
R. R. Dist.



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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
On
PCB Design & Fabrication
(08th & 09th January, 2019)
ATTENDANCE SHEET

S.No.	Name of the Participant	Programme	Year	Signature			
				Day-1 (08/01/2019)	Day-2 (09/01/2019)		
6.	JANA SANDHYA	B.Tech	II	FN	AN	FN	AN
7.	JANGA THARUN REDDY	B.Tech	II	FN	AN	FN	AN
8.	JANGATWAR TAGORE PRADEEP KUMAR	B.Tech	II	FN	AN	FN	AN
9.	KAKARLA MRUDULA	B.Tech	II	FN	AN	FN	AN
10.	KANCHARWAR RAHUL REDDY	B.Tech	II	FN	AN	FN	AN

Co Ordinator

Head of the Department
Global Institute of Engineering & Technology
Chilukur (V), Moinabad (M), R.R. Dist.T.S-501507

Principal
Global Institute of Engg. & Tech
Chilukur (V), Moinabad (M)
R. R. Dist



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SURVEY NO 179, CHILKUR(V), MOINABAD(M), RANGAREDDY DIST.T.S

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
On
PCB Design & Fabrication
(08th & 09th January, 2019)
ATTENDANCE SHEET

S.No.	Name of the Participant	Programme	Year	Signature			
				Day-1 (08/01/2019)	Day-2 (09/01/2019)		
6.	JANA SANDHYA	B.Tech	II	FN	AN	FN	AN
7.	JANGA THARUN REDDY	B.Tech	II	FN	AN	FN	AN
8.	JANGATWAR TAGORE PRADEEP KUMAR	B.Tech	II	FN	AN	FN	AN
9.	KAKARLA MRUDULA	B.Tech	II	FN	AN	FN	AN
10.	KANCHARWAR RAHUL REDDY	B.Tech	II	FN	AN	FN	AN

Co Ordinator

Head of the Department
Global Institute of Engineering & Technology
Chilukur (V), Moinabad (M), R.R. Dist.T.S-501507

Principal
Global Institute of Engg. & Tech
Chilukur (V), Moinabad (M)
R. R. Dist

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
On
PCB Design & Fabrication
(08th & 09th January, 2019)
ATTENDANCE SHEET

S.No.	Name of the Participant	Programme	Year	Signature			
				Day-1 (08/01/2019)	Day-2 (09/01/2019)		
				FN	AN	FN	AN
21.	THATIPALLY JEEVANAYOTHI	B.Tech	II	Sen	Sen	Sen	Sen
22.	TUMMALAPALLI N S LALITHA SREE ALEKHYA	B.Tech	II	Asst	Asst	Asst	Asst
23.	K DURGABHAVANI	B.Tech	II	K.Bhar	K.Bhar	K.Bhar	K.Bhar

Co Ordinator

20/01/2019

Head of the Department

20/01/2019

Department of Electronics & Communication Engg.
Global Institute of Engineering & Technology
Chilkur (V), Moinebad (M), R.R. Dist.T.S-501504



Principal
Global Institute of Engg. & Tech
Chilkur (V), Moinebad (M)
R. R. Dist

20/01/2019



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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
A Two Day Workshop
On
PCB Design & Fabrication
(08th & 09th January, 2019)

FEEDBACK FORM

Please valueate your rating of the course by placing a tick in the appropriate box.
1. Poor 2. Satisfactory 3. Good 4. Very good 5. Excellent

Branch and Year: *B. Tech IInd Year*

Date: *09/01/19*

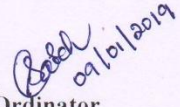
ASPECTS	RATING				
	Excellent 5	Very good 4	Good 3	Satisfactory 2	Poor 1
Relevance of contents	✓				
Trainer was knowledgeable and skillful		✓			
Quality of input provided	✓				
Quality of presentations		✓			
Adherence to the time schedule	✓				
Opportunity given to participant to clear doubts	✓				
Identify ways to build on current skills and knowledge	✓				
Overall learning experience	✓				
How has the course enhanced your skills or understanding of this topic?					
<div style="text-align: center;">—</div>					
Specify problems faced by you during the course?					
<div style="text-align: center;">—</div>					

**REPORT
ON
TWO DAY WORKSHOP:
PCB DESIGN & FABRICATION**

A Two day Workshop titled "PCB Design & Fabrication" was organized by department of Electronics & Communication Engineering OF GIET, HYDERABAD the workshop commenced with inaugural ceremony at 9:30 AM Ms. Sai Chandana, Assistant Professor ECE, GIET. Welcomed all the dignitaries present on the dais and colleagues & students. He mentioned about the significance of the workshop. Mr. Indranil Gupta, Technical Engineer, Coreel Technologies Pvt. Ltd. Hyderabad described the PCB and how it is connected to different fields with example. He motivated everyone to actively participate in the workshop to get benefited.

Day 1: The Technical session started at 10:00 AM, Mrs. Nuzath Unnisa Begum, Assistant Professor ECE, GIET. Delivered a topic on "Description of Electronic Component ". Mam discussed about description of Electronic Component. Mr. Ahmed Zeeshan, Assistant Professor & Head , GIET explained the "Introduction to PCB layout" in which sir explained about PCB layout like substrate parameters, thickness etc. from 11:30 AM to 1:00 PM. The next speaker Mr. Indranil Gupta, Technical Engineer, Coreel Technologies Pvt.Ltd., Hyd lectured on "Technologies, Materials, Software tools in PCB Design" which includes different technologies used for designing of PCB. He also discussed the types of substrates used and dielectric constants and various tools from 2:00 PM to 3:30 PM.

Day 2: Session started at 9:30 am, Dr. Ravi Kumar, Professor of ECE, Siddhartha Inst. of Engg. & Tech. delivered a lecture on "PCB Fabrication process". He discussed about PCB Fabrications steps and explained each step with its schematic diagram in his session. Followed by second lecture on "Testing and Trouble shouting" by Mr. Indranil Gupta, Technical Engineer, Coreel Technologies Pvt.Ltd, Hyderabad Both the sessions gives hands on experience of PCB Fabrication process. The workshop concludes with validity function, feedback and certificate distribution.


Co Ordinator
Ms. M.Sai Chandana
Assistant Professor
Department of ECE



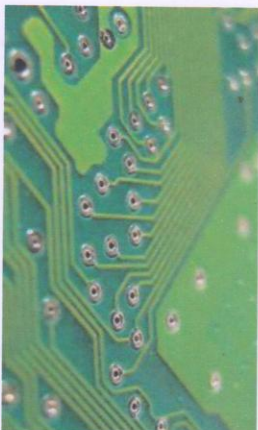
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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING



*A Two Day Workshop
On*

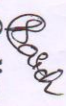
PCB DESIGN & FABRICATION
(08th & 09th January 2019)


CERTIFICATE OF PARTICIPATION

This is to certify that Ms./Mr. RACHA SRUTHI

bearing roll number 17UG1A0420 has attended a Two Day Workshop on "PCB DESIGN & FABRICATION"

held on 08th January 2019 & 09th January 2019 at Global Institute of Engineering and Technology.


Co-Ordinator
(M. Sai Chandana)

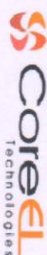

Head of the Department
(G. Ahmed Zeshan)


Principal
(Dr. K. Sivalingam)



GLOBAL

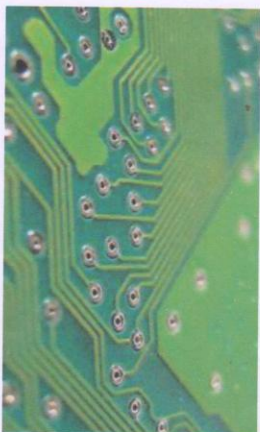
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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING



A Two Day Workshop

On

PCB DESIGN & FABRICATION

(08th & 09th January 2019)

CERTIFICATE OF PARTICIPATION

This is to certify that Ms./Mr. ARACHT JOHNSON

bearing roll number 17U61A0401 has attended a Two Day Workshop on "PCB DESIGN & FABRICATION"

held on 08th January 2019 & 09th January 2019 at Global Institute of Engineering and Technology.

Co-Ordinator
(M. Sai Chandana)

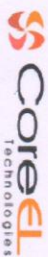
Head of the Department
(G. Ahmed Zeeshan)

Principal
(Dr. K. Sivalingam)



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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING



A Two Day Workshop

On

PCB DESIGN & FABRICATION

(08th & 09th January 2019)

CERTIFICATE OF PARTICIPATION

This is to certify that Ms./Mr. _____

GADDAM RAKESH

bearing roll number 17UG1A0404 has attended a Two Day Workshop on "PCB DESIGN & FABRICATION"

held on 08th January 2019 & 09th January 2019 at Global Institute of Engineering and Technology.

M. Sai Chandana
Co-Ordinator
(M. Sai Chandana)

G. Ahmed Zeeshan
Head of the Department
(G. Ahmed Zeeshan)

Dr. K. Sivalingam
Principal
(Dr. K. Sivalingam)