



# GLOBAL Institute of Engineering & Technology

(Approved by AICTE & Affiliated to JNTUH)

Survey No. 179, Chilkur (V), Moinabad (M), Ranga Reddy Dist. TS.

Phone: 08417-252233 / 253021

e-mail: [principal.giet.u6@gmail.com](mailto:principal.giet.u6@gmail.com)

JNTUH Code(U6) CIVIL – CSE – MECH – ECE – EEE – MBA – M.Tech. EAMCET Code GLOB

## Department of Electronics and Communication Engineering

### G Ahmed Zeeshan

BE.,M.Tech.,MBA.,(Ph.D)

MISTE.,MISOL.,MIEEEE(USA)

### Assistant Professor & Head

Lr.No: GIET/ECE/Compt Ex/001/09/2018-19

Date 08/09/2018

To

The Principal  
Global Institute of Engineering & Technology  
Moinabad

Sir,

Sub: Permission to Conduct Competitive Exam Coaching for Students of ECE-III & IV Yr

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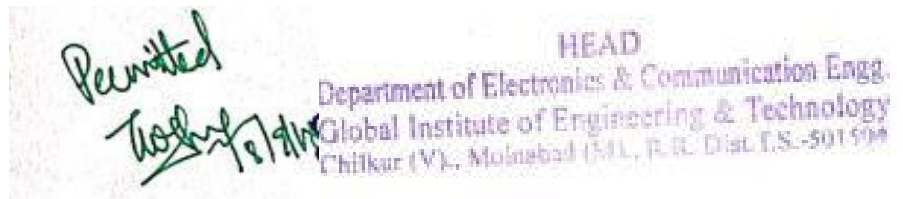
This is to get it to your kind notice that we Department of Electronics and Communication Engineering are interested to conduct a Coaching for competitive exams to the students of III & IV year, which is undertaken for students benefit as a value addition to curriculum of course.

These Coaching classes help students to strengthen their basics of courses learnt from first year to final year and help them to crack various competitive exams.

Kindly accord approval so as to make necessary arrangements for starting the classes for the students of ECE- III & IV yr Academic Year: 2018-19.

Thanking you.

**HEAD**



Cc to :

**Director-** for information

**Dean** – for information

**Heads** – CE,EEE,ME,ECE,CSE - for information



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**Department of Electronics and Communication Engineering**

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**G Ahmed Zeeshan**

BE.,M.Tech.,MBA.,(Ph.D)  
MISTE.,MISOI.,MIEEEE(USA)

**Assistant Professor & Head**

## **DEPARTMENT CIRCULAR**

Cr.No: GIET/ECE/Compt Ex/004/09/2018-19

Date 15/09/2019

Department of Electronics and Communication Engineering is conducting Competitive Exam Coaching classes for Students of B.Tech ECE-III & IV Yr from **18/09/2018**, in association with Sharada Coaching Academy- Hyderabad in Room No.39. The Co-ordinator for the Competitive Exam Coaching classes is Mr.S Ravi, Assistant Professor, Department of ECE. Students who are interested can contact the coordinator and give your consent by tomorrow.

**Head**

HEAD

Department of Electronics & Communication Engg.  
Global Institute of Engineering & Technology  
Chilkur (V), Moinabad (M), R.R. Dist.T.S.-501504

**Cc to :**

**ECE Department** – for filing & Display on Department Notice Board

**Mentors ECE-III&IV** – for informing students and post message in Class Group

**Coordinator-** for information



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## Department of Electronics and Communication Engineering

**G Ahmed Zeeshan**

BE.,M.Tech.,MBA.,(Ph.D)  
MISTE.,MISOI.,MIEEE(USA)

**Assistant Professor & Head**

### DEPARTMENT CIRCULAR

Cr.No: GIET/ECE/Compt Ex/005/01/2018-19

Date 30/01/2019

Upon Successful Completion of Competitive Examinations Training for the current Academic Year, it is hereby informed that GATE/PGECET Mock Exam will be conducted on 02/02/2019 (FN) and TOEFLE/PTE/IELTS Mock Exam will be conducted on 02/02/2019 (AN) . In this regard all the students of B.Tech ECE- III &IV Year are here by informed to attend Examination.

For further details contact Competitive Examinations Co-ordinator .

**Head**

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Global Institute of Engineering & Technology  
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**Coordinator-** for information



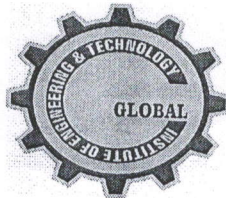
SHARADHA  
COACHING ACADEMY



GLOBAL  
Institute of Engineering & Technology

# MEMORANDUM OF UNDERSTANDING

Between

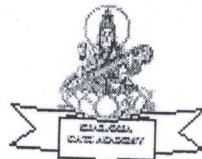


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Dist. TS.

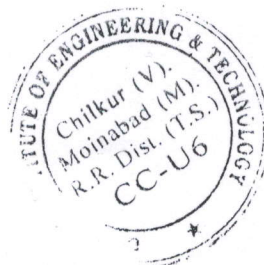
&



**SHARADHA  
COACHING ACADEMY**

Hyderabad

6-9-12/16A, Ramachandrapuram colony, IS Sadan, Saidabad, Hyderabad-500059





SHARADHA  
COACHING ACADEMY



GLOBAL  
Institute of Engineering & Technology

## MEMORANDUM OF UNDERSTANDING

Between

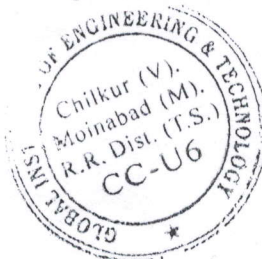
### Global Institute of Engineering & Technology, Moinabad & Sharadha Coaching Academy, Hyderabad

This Memorandum of understanding (“MOU”) is entered into on this date **19<sup>th</sup> AUGUST 2017** by and between Global Institute of Engineering & Technology, Moinabad and Sharadha Coaching Academy, Hyderabad (**ACADEMY** and **COLLEGE** are here in after referred to individually as “Party” or collectively as “Parties”).

This Memorandum of Understanding has been drawn up between **ACADEMY** and **COLLEGE** to provide **Academic support by conducting Competitive Exam & CRT Training classes** for our Third & Fourth Year students, for possible joint initiatives, With the purpose of supporting the common goals of both managements, enabling collaborative actions to be planned and implemented, and making effective use of the complementary strengths of both organizations.

**Academy** pledges to support **COLLEGE** in this regard and provide it with advice and resources within its means to ensure successful implementation of conducting Competitive Exam & CRT Coaching Classes in Campus with a stipulated schedule and achievement of its objectives.

In Case **ACADEMY** and **COLLEGE** have differences concerning the implementation of the Competitive Exam & CRT Coaching Classes they undertake to resolve them through dialogue and discussion. In case such differences cannot be resolved either party can terminate this Memorandum of Understanding by giving **FIFTEEN DAYS** notice in writing to the other party and make such termination public only after it has taken effect. This Memorandum of Understanding will remain in effect unless terminated according to the above provision.





**SHARADHA  
COACHING ACADEMY**



**GLOBAL  
Institute of Engineering & Technology**

**Rules and Regulations:**

- All the classes of concerned modules are to be conducted only in the college campus by eminent trainers, after college hours.
- The Academy should not demand any amount from the student other than the honorarium provided by the college.
- If any dispute or difference of any kind whatsoever where to arise between INSTITUTION and COLLEGE, authorized Coordinator of the INSTITUTION may Approach only the Principal of COLLEGE.

The implementation of this MOU shall be renewed annually & however the MOU will be in force for 5 years.

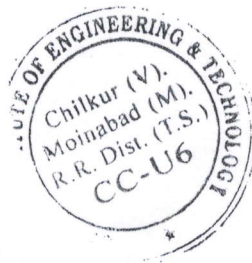
Now, therefore this Memorandum of Understanding is signed on this Date: 19<sup>th</sup> August 2017, subject to such corporate approvals deemed necessary and shall remain in effect unless terminated in accordance with the above provision.

**For and on behalf of**

*D S Prasad.*  
**Dr. D.S. Prasad  
General Manager  
Sharadha Coaching Academy**

*V.V.V.S. Murthy*  
**Dr. V.V.V.S. Murthy  
Principal  
Global Institute of Engineering  
& Technology  
PRINCIPAL**

Global Institute of Engg. & Tech  
Chilkur (V), Moinabad (M)





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## Department of Electronics and Communication Engineering

### Time Table for Competitive Exams Training/CRT Classes A.Y :2018-19

S.No.	Day	Year	Name of the Training Programme
1	Monday & Tuesday	ECE-III, IV	GATE/PGECET
2	Wednesday & Thursday	ECE-II, III, IV	CRT
3	Friday & Saturday	ECE-III, IV	IELTS, TOEFL, PTE, GRE



Coordinator



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## Competitive Exam Training Students of ECE-III & IV Yr Academic Year : 2018-19

### SYLLABUS for TOEFL/IELTS/PTE

Schedule : Friday & Saturday

Time: 4:00 PM-5.00 PM

#### MODULE 1: (8 WEEKS)

S.No.	Subjects	Duration (in Weeks)	Name of the Instructor
1	Listening	2	Mr.Vaman Rao,
2	Speaking	2	Mrs.Mangaveni
3	Analytical Writing	2	Mohammed Maqbool Ali
4	Verbal Reasoning	2	Mr.Shailesh Yadav

#### MODULE 2: (6 WEEKS)

S.No.	Subjects	Duration (in Weeks)	Name of the Instructor
1	Reading	1	Mr.Krupakar Bharani
2	Writing	1	Mr.Sathya Narayana.P
3	Quantitative Reasoning	2	Mrs.Manjula.G
4	AWA	2	Mr.Rama Chandra.P
	<b>Mock Exam</b>		

Co-ordinator

HEAD

HEAD  
Department of Electronics & Communication Engg.  
Global Institute of Engineering & Technology  
Chilkur (V), Moinabad (M), R.R. Dist.T.S.-501599



**Competitive Exam Training Students of ECE-III & IV Yr**  
**Academic Year : 2018-19**

**TOEFL Syllabus:**

1. Syllabus for listening: In this part of exam, aspirants need to be very active and alert.

Here are some topics that you must concentrate at while preparing for the exam. This

**Part Of The Exam Involves:**

Listening To Lectures

Pay attention to classroom discussions & conversations

Answering questions in classrooms

2. Syllabus for Reading: In this section of the TOEFL exam syllabus 2022, there will be 3 or 4 passages from academic texts and answering questions. In this section, only reading ability of the candidate will be tested and passage can come from anywhere.

A better preparation of this part can be done by practicing reading passages from books or papers.

3. Syllabus for Speaking: In this section of the syllabus, candidate will be asked to express an opinion on a familiar topic as provided to them. The main objective of this test is to analyze the understanding ability of aspirants.

To speak and explain the particular topic as per provided to you in the task.

4. Syllabus for Writing: In this part of the exam, candidates are required to write essay responses based on reading and listening tasks. In order to crack the competitive exam, you have to well inform.



**Co-ordinator**



**HEAD**

**HEAD**  
Department of Electronics & Communication Engg.  
Global Institute of Engineering & Technology  
Chilkur (V), Moinabad (M), R.R. Dist. T.S.-501509



### Competitive Exam Training Students of ECE-III & IV Yr

Academic Year : 2018-19

### SYLLABUS for GATE/PGCET/GRE

Schedule: Monday & Tuesday Time: 4:00 PM - 5.00 PM

Total No.of Courses	14
No.of Modules	4
Courses per Module 1 ( III Yr-I Sem)	4
Courses per Module 2 ( III Yr-II Sem)	5
Courses per Module 3 ( IV Yr-I Sem)	3
Courses per Module 4 ( IV Yr-II Sem)	2

Module -I			
S.No	Course	Weeks	Faculty
1	Networks	2	Dr. S. Padam Priyal, GIET
2	Signals and Systems	2	Mr.Chenna Kesavulu, SCA
3	Electronic Devices	2	Dr. Challa Venkateshwara Rao, GIET
4	Analog Circuits	2	Mr.Virender, SCA

Module -II			
S.No	Course	Weeks	Faculty
1	Digital Circuits	2	Mrs.Kiranmay, SCA
2	Aptitude-1	2	Mr.Sai Baba, SCA
3	Oscillators, 555 Timer	1	Dr. S Padam Priyal,GIET
4	Microprocessors & Microcontrollers	1	Dr.P.Markandeya Raju,VGI
5	Control Systems	1	Mrs.Manasa, SCA

Module -III			
S.No	Course	Weeks	Faculty
1	Analog Communications	3	Mr.Chaitanya,SCA
2	Digital Communications	2	Dr.Prathap Reddy, SCA
3	Electromagnetics	3	Dr.Chandu Subba Rao, SCA



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## Department Of Electronics and Communication Engineering

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Module -IV			
S.No	Course	Weeks	Faculty
1	Aptitude-2	3	Mr.Sai Baba, SCA
2	English Grammar	3	Mr.Satya Narayana,GIET
3	Mock Gate Exam		

**Co-Ordinator**

**Head**

**HEAD**  
Department of Electronics & Communication Engg.  
Global Institute of Engineering & Technology  
Chilkur (V), Molasebad (M.L), R.R. Dist. T.S.-501504



## **GATE EC Syllabus 2018-19**

**General Aptitude(GA):** Common Syllabus for all papers

The GATE EC Syllabus consists of GA section which will follow the same pattern of questions and marking scheme for all the papers of GATE 2017. This section is considered to be easy and will test your English skills and general numeric ability.

**Verbal Ability:** English grammar, sentence completion, verbal analogies, word groups, instructions, critical reasoning and verbal deduction.

**Numerical Ability:** Numerical computation, numerical estimation, numerical reasoning and data interpretation.

### **Section 1: Engineering Mathematics**

This section of [GATE](#) EC Syllabus consists of testing your practical and theoretical skills that are typically used in the engineering industry.

- Linear Algebra:** Vector space, basis, linear dependence and independence, matrix algebra, eigen values and eigen vectors, rank, solution of linear equations – existence and uniqueness.
- Calculus:** Mean value theorems, theorems of integral calculus, evaluation of definite and improper integrals, partial derivatives, maxima and minima, multiple integrals, line, surface and volume integrals, Taylor series.
- Differential Equations:** First order equations (linear and nonlinear), higher order linear differential equations, Cauchy's and Euler's equations, methods of solution using

variation of parameters, complementary function and particular integral, partial differential equations, variable separable method, initial and boundary value problems.

- **Vector Analysis:** Vectors in plane and space, vector operations, gradient, divergence and curl, Gauss's, Green's and Stoke's theorems.
- **Complex Analysis:** Analytic functions, Cauchy's integral theorem, Cauchy's integral formula; Taylor's and Laurent's series, residue theorem.
- **Numerical Methods:** Solution of nonlinear equations, single and multi-step methods for differential equations, convergence criteria.
- **Probability and Statistics:** Mean, median, mode and standard deviation; combinatorial probability, probability distribution functions – binomial, Poisson, exponential and normal; Joint and conditional probability; Correlation and regression analysis.

## Section 2: Networks, Signals and Systems

- **Network solution methods:** nodal and mesh analysis; Network theorems: superposition, Thevenin and Norton's, maximum power transfer; Wye-Delta transformation; Steady state sinusoidal analysis using phasors; Time domain analysis of simple linear circuits; Solution of network equations using Laplace transform; Frequency domain analysis of RLC circuits; Linear 2-port network parameters: driving point and transfer functions; State equations for networks.
- **Continuous-time signals:** Fourier series and Fourier transform representations, sampling theorem and applications; Discrete-time signals: discrete-time Fourier transform (DTFT), DFT, FFT, Z-transform, interpolation of discrete-time signals; LTI systems: definition and properties, causality, stability, impulse response, convolution, poles and zeros, parallel and cascade structure, frequency response, group delay, phase delay, digital filter design techniques.

## Section 3: Electronic Devices

- **Energy bands in intrinsic and extrinsic silicon; Carrier transport:** diffusion current, drift current, mobility and resistivity; Generation and recombination of carriers; Poisson and continuity equations; P-N junction, Zener diode, BJT, MOS capacitor, MOSFET, LED, photo diode and solar cell; Integrated circuit fabrication process: oxidation, diffusion, ion implantation, photolithography and twin-tub CMOS process.

## Section 4: Analog Circuits

- Small signal equivalent circuits of diodes, BJTs and MOSFETs; Simple diode circuits: clipping, clamping and rectifiers; Single-stage BJT and MOSFET amplifiers: biasing, bias stability, mid-frequency small signal analysis and frequency response; BJT and MOSFET amplifiers: multi-stage, differential, feedback, power and operational; Simple op-amp circuits; Active filters; Sinusoidal oscillators: criterion for oscillation, single-transistor and opamp configurations; Function generators, wave-shaping circuits and 555 timers; Voltage reference circuits; Power supplies: ripple removal and regulation.

## Section 5: Digital Circuits

- Number systems; Combinatorial circuits: Boolean algebra, minimization of functions using Boolean identities and Karnaugh map, logic gates and their static CMOS implementations, arithmetic circuits, code converters, multiplexers, decoders and PLAs; Sequential circuits: latches and flip-flops, counters, shift-registers and finite state machines; Data converters: sample and hold circuits, ADCs and DACs; Semiconductor memories: ROM, SRAM, DRAM; 8-bit microprocessor (8085): architecture, programming, memory and I/O interfacing.

## Section 6: Control Systems

- Basic control system components; Feedback principle; Transfer function; Block diagram representation; Signal flow graph; Transient and steady-state analysis of LTI systems; Frequency response; Routh-Hurwitz and Nyquist stability criteria; Bode and root-locus plots; Lag, lead and lag-lead compensation; State variable model and solution of state equation of LTI systems.

## Section 7: Communications

- Random processes: autocorrelation and power spectral density, properties of white noise, filtering of random signals through LTI systems; Analog communications: amplitude modulation and demodulation, angle modulation and demodulation, spectra of AM and FM, superheterodyne receivers, circuits for analog communications; Information

theory: entropy, mutual information and channel capacity theorem; Digital communications: PCM, DPCM, digital modulation schemes, amplitude, phase and frequency shift keying (ASK, PSK, FSK), QAM, MAP and ML decoding, matched filter receiver, calculation of bandwidth, SNR and BER for digital modulation; Fundamentals of error correction, Hamming codes; Timing and frequency synchronization, inter-symbol interference and its mitigation; Basics of TDMA, FDMA and CDMA.

## Section 8: Electromagnetics

- Electrostatics; Maxwell's equations: differential and integral forms and their interpretation, boundary conditions, wave equation, Poynting vector; Plane waves and properties: reflection and refraction, polarization, phase and group velocity, propagation through various media, skin depth; Transmission lines: equations, characteristic impedance, impedance matching, impedance transformation, S-parameters, Smith chart; Waveguides: modes, boundary conditions, cut-off frequencies, dispersion relations; Antennas: antenna types, radiation pattern, gain and directivity, return loss, antenna arrays; Basics of radar; Light propagation in optical fibers.



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**Department of Electronics and Communication Engineering**

## Competitive Exam Training for Students of ECE-III & IV Yr

Academic Year : 2018-19

### LIST OF STUDENTS ENROLLED

S.NO.	ROLL NUMBER	NAME OF THE STUDENT	YEAR
1	15U61A0401	ALLU SOMESH	IV
2	15U61A0405	BODDU DIVYA	IV
3	15U61A0406	DEVISETTY HEMANTH	IV
4	15U61A0408	KADIRE PAVANI REDDY	IV
5	15U61A0409	KAMBAM BHAVANI	IV
6	15U61A0410	KARRA MANOJ	IV
7	15U61A0412	KEDIKA SHUSHANTH REDDY	IV
8	15U61A0414	KODARI PRAVEEN REDDY	IV
9	15U61A0415	KOLUGURI SREEJA	IV
10	15U61A0416	KUNTE PRIYANKA	IV
11	15U61A0418	MOHAMMED MOSAAD LABABIDI	IV
12	15U61A0420	NYALAM BHANUCHANDAR	IV
13	15U61A0428	VINAY UNİYAL	IV
14	15U61A0413	KODALI SAI NAVEEN	III
15	15U61A0417	L SRIKANTH REDDY	III
16	15U61A0427	VEM VAMSHI	III
17	16U61A0401	AFREEN BEGUM	III
18	16U61A0405	BADDAMOLLA SHIREESHA	III
19	16U61A0410	DESIBOYINA BHARGAV SAI	III
20	16U61A0413	JANGAM VIKAS KUMAR	III
21	16U61A0414	KAUSER BEGUM	III
22	16U61A0418	MOHD KAZIM SHARIF	III
23	16U61A0419	N SOWMYA	III
24	16U61A0421	NANDAMURI NARENDRA	III
25	16U61A0424	SHEELAM RAMAKRISHNA	III
26	16U61A0425	SUMMAIYA	III
27	17U65A0401	A YAMUNA	III
28	17U65A0402	KARUPURAM BHAVANA	III
29	17U65A0403	MOHAMMED ABDUL BARI KHAN	III





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**Department of Electronics and Communication Engineering**

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**Co-Ordinator**

Mr.S Ravi

Assistant Professor

Department of ECE

**Head**

HEAD

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## Department of Electronics and Communication Engineering

### Competitive Exam Training/CRT for Students of ECE-III & IV Yr Academic Year : 2018-19

S.NO	ROLL NUMBER	NAME OF THE STUDENT	YEAR	MARKS										
				1	2	3	4	5	6	7	8	9	10	
1	15U61A0401	ALLU SOMESH	IV	1	2	3	4	5	0	6	7	8	9	9
2	15U61A0405	BODDU DIVYA	IV	1	2	3	0	4	5	6	7	8	9	9
3	15U61A0406	DEVISETTY HEMANTH	IV	1	2	3	4	5	6	7	8	9	10	10
4	15U61A0408	KADIRE PAVANI REDDY	IV	1	2	3	4	5	6	7	8	9	10	10
5	15U61A0409	KAMBAM BHAVANI	IV	1	2	0	3	4	5	6	7	8	9	9
6	15U61A0410	KARRA MANOJ	IV	1	2	3	4	0	5	6	7	8	9	9
7	15U61A0412	KEDIKA SHUSHANTH REDDY	IV	1	2	3	4	5	6	7	8	9	0	9
8	15U61A0414	KODARI PRAVEEN REDDY	IV	1	2	3	4	5	6	7	8	9	10	10
9	15U61A0415	KOLUGURI SREEJA	IV	1	2	3	4	5	6	7	8	9	10	10
10	15U61A0416	KUNTE PRIYANKA	IV	1	2	3	4	5	6	7	8	9	10	10
11	15U61A0418	MOHAMMED MOSAAD LABABIDI	IV	1	2	3	4	5	6	7	8	9	10	10
12	15U61A0420	NYALAM BHANUCHANDAR	IV	1	2	3	4	5	6	7	8	9	10	10
13	15U61A0428	VINAY UNIYAL	IV	1	2	3	4	5	6	7	8	9	10	10
14	15U61A0413	KODALI SAI NAVEEN	III	1	2	3	4	5	6	7	8	9	10	10
15	15U61A0417	L SRIKANTH REDDY	III	1	2	3	4	5	6	7	8	9	10	10
16	15U61A0427	VEM VAMSHI	III	1	2	3	4	5	6	7	8	9	10	10
17	16U61A0401	AFREEN BEGUM	III	1	2	3	4	5	6	7	8	9	10	10
18	16U61A0405	BADDAMOLLA SHIREESHA	III	1	2	3	4	5	6	7	8	9	10	10
19	16U61A0410	DESIBOYINA BHARGAV SAI	III	1	2	3	4	5	6	7	8	0	9	9



# GLOBAL INSTITUTE OF ENGINEERING & TECHNOLOGY

(Approved by AICTE & Affiliated to JNTUH)

Survey No. 179, Chilkur (V), Moinabad (M), Ranga Reddy Dist. TS.

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JNTUH Code (U6)

CIVIL - CSE - MECH - ECE - EEE - MBA - M.Tech. EAMCET Code- GLOB

## Department of Electronics and Communication Engineering

20	16U61A0413	JANGAM VIKAS KUMAR	III	1	2	3	4	5	6	7	8	9	10	10
21	16U61A0414	KAUSER BEGUM	III	1	0	2	3	4	5	6	7	8	9	9
22	16U61A0418	MOHD KAZIM SHARIF	III	1	2	3	4	5	6	7	8	9	10	10
23	16U61A0419	N SOWMYA	III	1	2	3	4	5	6	7	8	9	10	10
24	16U61A0421	NANDAMURI NARENDRA	III	1	2	3	4	5	6	7	8	9	10	10
25	16U61A0424	SHEELAM RAMAKRISHNA	III	1	2	3	4	5	6	7	8	9	10	10
26	16U61A0425	SUMMAIYA	III	1	2	0	3	4	5	6	7	8	9	9
27	17U65A0401	A YAMUNA	III	1	2	3	4	5	6	7	8	9	10	10
28	17U65A0402	KARUPURAM BHAVANA	III	1	2	3	4	5	6	7	8	9	10	10
29	17U65A0403	MOHAMMED ABDUL BARI KHAN	III	1	2	0	3	4	5	6	7	8	9	9

Co-Ordinator

Head

HEAD  
Department of Electronics & Communication Engg.  
Global Institute of Engineering & Technology  
Chilkur (V), Moinabad (M), R.R. Dist. T.S.-501504



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Recognized Under 2f Act of UGC 1956, an IS:9001-2015 Certified Institution

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EAMCET Code -

GLOB

## Department Of Electronics And Communication Engineering

Competitive Exam Training for Students of ECE II&IV Yr  
Academic Year : 2018-19

### FEEDBACK FORM

Please evaluate your rating of the course by placing a tick in the appropriate box.

1. Poor 2. Satisfactory 3. Good 4. Very good 5. Excellent

Date: 20/02/2019

ASPECTS	RATING				
	Excellent 5	Very good 4	Good 3	Satisfactory 2	Poor 1
Coverage of important topics	✓				
Trainer was knowledgeable and skillful	✓				
Quality of input provided		✓			
Presentation in class	✓				
Adherence to the time schedule	✓				
Opportunity given to clear doubts	✓				
Overall learning experience	✓				
How did the training classes enhance your skills?					
<i>Learned a lot</i>					
Specify problems faced by you during the training classes?					
<i>No problem</i>					



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## Department of Electronics and Communication Engineering

### A Report of Guidance on Competitive Exams Academic Year : 2018-19

As a quality Initiative, Department of Electronics and Communication Engineering in association with Sharada Coaching Academy has duly conducted Competitive Exam Training Classes for student UG-ECE-III, and IV years for Academic Year 2018-2019 from 18-09-2018 to 20-02-2019 from 4.00 PM to 5.00 PM.

A circular was given to class mentor before 1<sup>st</sup> Midterm Examination to prepare list of Fast & Slow Learners, based on student's performance in Formative Assessments, Midterm Results and in consultation with respective Course Faculty. A List of Fast & Slow learners is submitted by respective Class Mentors to the Head, through Head Mentor.

A Week after Commencement of 1<sup>st</sup> Midterm Examinations, students were given a circular regarding conduct of Competitive Exam Training Classes.



Modules were designed for Various competitive exams like GATE, IES, TOEFL, GRE , PGCET, IELTS, PTE. Time tables for these classes were made such that training for National Level Competitive Examinations were given on Mondays & Tuesdays and training for

International Level Competitive Examinations for aspirants were given on Fridays & Saturdays, by eminent faculty of GIET in association with Sharada Coaching Academy.

Total 29 Students, of which 13 students from IV Year, 16 students from III Year benefited through Competitive Exams Training classes.



### **Guidance for Competitive Examinations**

Day to day monitoring & conduct of classes was monitored by the coordinator of Competitive Examinations & Attendance was taken at the end of classes scheduled from 4 PM to 5 PM. important topics were covered from course. Students were also made to practice previous papers of Competitive Examinations.



### **Students are writing Mock Test**

At the end of training, a circular was sent to students of final year for writing a Mock Examination. Mock Examinations were conducted and results of the same were announced. Feedbacks were collected from students at the end of the training.

This initiative has proved to be a Booster in performance of Fast Learners in Various National & International Level Competitive Examinations.



**COORDINATOR**



**HEAD**

**HEAD**  
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Global Institute of Engineering & Technology  
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