



## GLOBAL INSTITUTE OF ENGINEERING & TECHNOLOGY

(Approved by AICTE & Affiliated to JNTUH)

Survey No. 179, Chilkur (V), Moinabad (M), Ranga Reddy Dist. T.S.

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JNTUH Code (U6)

ECE – EEE – CSE - CIVIL – ME – MBA - M.Tech

EAMCET code (GLOB)

### ELECTRICAL AND ELECTRONICS ENGINEERING

**Dr.T.V.V Pavan Kumar**

B.Tech, M.Tech, Ph.D.

Professor & Head

Date: 14/08/2017

### CIRCULAR

The Department of Electrical and Electronics Engineering is hosting A *Three Day Workshop on Power Electronics converters and it's Application* for the students of IV Year B.Tech Electrical and Electronics Engineering in Room No. 04 from 28<sup>th</sup> August 2017 to 30<sup>th</sup> August 2017. All the students of II B. Tech are hereby informed to register for the course on or before 26<sup>th</sup> August 2017. The Coordinator for the course is Mr. Issac, Assistant Professor, Department of Electrical and Electronics Engineering.

Cc to:  
Principal  
Co ordinator IQAC  
Mentors  
Training & Placement Cell  
Notice board  
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Department of Electrical & Electronics En  
Global Institute of Engineering & Technolo  
Chilkur (V), Moinabad (M), R.R. Dist. T.S.-5013



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ELECTRICAL AND ELECTRONICS ENGINEERING

## A Three Day Workshop

On

Power Electronics converters and it's Application  
(28<sup>th</sup> – 30<sup>th</sup> August, 2017)

### PROGRAMME SCHEDULE

	TIME/LENGTH	SESSION	SPEAKER
Day 1	09:30 AM - 10:00 AM	Inauguration	
	10:00 AM - 11:00 AM	Introduction to ' Power Electronics converters and it's Application'	Mr G Venu, CVSR, Hyderabad
	11:00 AM - 11:15 AM	High Tea	
	11:15 AM - 01:00 PM	Basis Concepts of Power Electronics converters and it's Application	Mr G Venu, CVSR, Hyderabad
	01:00 PM - 02:00 PM	LUNCH BREAK	
	02:00 PM - 03:30 PM	PPT on Various Devices of Power Electronics converters and it's Application	Mr G Venu, CVSR, Hyderabad
Day 2	09:30 AM - 11:00 AM	Hands on Power Electronics converters and it's Application	Mr.G.Rajesh, Assistant Professor,GIET.
	11:00 AM - 11:15 AM	High Tea	
	11:15 AM - 01:00 PM	Construction of Power Electronics converters	Mr.G.Rajesh, Assistant Professor,GIET.
	01:00 PM - 02:00 PM	LUNCH BREAK	
	02:00 PM - 03:30 PM	Control techniques of Power Electronics converters	Mr.G.Rajesh, Assistant Professor,GIET.
Day 3	09:30 AM - 11:00 AM	Applications of power Electronic Converters	Mr G Venu, CVSR, Hyderabad
	11:00 AM - 11:15 AM	High Tea	
	11:15 AM - 01:00 PM	Advantages and Disadvantages of converters	Mr G Venu, CVSR, Hyderabad
	01:00 PM - 02:00 PM	LUNCH BREAK	
	02:00 PM - 03:30 PM	Valedictory	

Co- Ordinator  
Mr.Issac,  
Department of EEE



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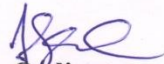
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Department of Electrical and Electronics Engineering

**A Three Day Workshop  
On  
Power Electronics converters and its Application  
(28<sup>th</sup> –30<sup>th</sup> August 2017)  
Participants List**

S No	H T No	Name of the Student
1	15U61A0205	MADISHETTI PRASHANTH
2	15U61A0206	MD BELAL
3	15U61A0207	MD NAFIS ALAM
4	15U61A0208	MD RAJA HASAN
5	15U61A0209	MOHAMMED AHMED
6	15U61A0212	PUSHPARAJ SINH
7	15U61A0214	SK NAZIMUL HAQUE
8	16U65A0202	BETHI AKANKSHA
9	16U65A0203	CH. PAVAN
10	16U65A0204	DOUPATI VIGNESHWARI
11	16U65A0205	MARUGOLU SIRISHA

  
**Co-Ordinator**  
Mr. Issac,  
Assistant Professor,  
Department of EEE



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A Three Day Workshop  
On

Power Electronics converters and it's Application

(28<sup>th</sup> -30<sup>th</sup> August 2017)

**REGISTRATION FORM**

(Please Fill in CAPITAL LETTERS)

Name : na dgetti prashanth  
Roll No : 15U61A0205  
Course/Year : B.tech/IV Department : EEE  
Phone No : 9246235678 E-mail : prashanthprashu@gmail.com  
Signature of Participant

For Office use only

Approved/ Not Approved

Signature of Coordinator



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A Three Day Workshop  
On

Power Electronics converters and it's Application

(28<sup>th</sup> -30<sup>th</sup> August 2017)

**REGISTRATION FORM**

(Please Fill in CAPITAL LETTERS)

Name : DOUPATI VIGNESHWARI  
Roll No : 16U65A0204  
Course/Year : B.tech Department : EEE  
Phone No : 9256123456 E-mail : vigneshwaridoupati@gmail.com  
Signature of Participant

For Office use only

Approved/ Not Approved

Signature of Coordinator



11	16U65A0205	MARUGOLU SIRISHA	B.Tech	IV					
8	1809A0305	BELLE VIKRANTH	B.Tech	IA					
9	1809A0306	2K AVANISH HVGLE	B.Tech	IA					
10	1809A0307	3GADHARVA SIBH	B.Tech	IA					
11	1809A0308	4DRAVINDS VIVIED	B.Tech	IA					
12	1809A0309	5D EATH HVZVA	B.Tech	IA					
13	1809A0310	6D AVISH VIVVA	B.Tech	IA					
14	1809A0311	7D BEIVE	B.Tech	IA					
15	1809A0312	8VDRISHLI MVZHVVALH	B.Tech	IA					
16	1809A0313	9DRAVINDS VIVVED	B.Tech	IA					
17	1809A0314	10D AVISH VIVVA	B.Tech	IA					
18	1809A0315	11D BEIVE	B.Tech	IA					
19	1809A0316	12VDRISHLI MVZHVVALH	B.Tech	IA					
20	1809A0317	13D AVISH VIVVA	B.Tech	IA					
21	1809A0318	14D BEIVE	B.Tech	IA					
22	1809A0319	15VDRISHLI MVZHVVALH	B.Tech	IA					
23	1809A0320	16D AVISH VIVVA	B.Tech	IA					
24	1809A0321	17D BEIVE	B.Tech	IA					
25	1809A0322	18VDRISHLI MVZHVVALH	B.Tech	IA					
26	1809A0323	19D AVISH VIVVA	B.Tech	IA					
27	1809A0324	20D BEIVE	B.Tech	IA					
28	1809A0325	21VDRISHLI MVZHVVALH	B.Tech	IA					
29	1809A0326	22D AVISH VIVVA	B.Tech	IA					
30	1809A0327	23D BEIVE	B.Tech	IA					
31	1809A0328	24VDRISHLI MVZHVVALH	B.Tech	IA					
32	1809A0329	25D AVISH VIVVA	B.Tech	IA					
33	1809A0330	26D BEIVE	B.Tech	IA					
34	1809A0331	27VDRISHLI MVZHVVALH	B.Tech	IA					
35	1809A0332	28D AVISH VIVVA	B.Tech	IA					
36	1809A0333	29D BEIVE	B.Tech	IA					
37	1809A0334	30VDRISHLI MVZHVVALH	B.Tech	IA					
38	1809A0335	31D AVISH VIVVA	B.Tech	IA					
39	1809A0336	32D BEIVE	B.Tech	IA					
40	1809A0337	33VDRISHLI MVZHVVALH	B.Tech	IA					
41	1809A0338	34D AVISH VIVVA	B.Tech	IA					
42	1809A0339	35D BEIVE	B.Tech	IA					
43	1809A0340	36VDRISHLI MVZHVVALH	B.Tech	IA					
44	1809A0341	37D AVISH VIVVA	B.Tech	IA					
45	1809A0342	38D BEIVE	B.Tech	IA					
46	1809A0343	39VDRISHLI MVZHVVALH	B.Tech	IA					
47	1809A0344	40D AVISH VIVVA	B.Tech	IA					
48	1809A0345	41D BEIVE	B.Tech	IA					
49	1809A0346	42VDRISHLI MVZHVVALH	B.Tech	IA					
50	1809A0347	43D AVISH VIVVA	B.Tech	IA					
51	1809A0348	44D BEIVE	B.Tech	IA					
52	1809A0349	45VDRISHLI MVZHVVALH	B.Tech	IA					
53	1809A0350	46D AVISH VIVVA	B.Tech	IA					
54	1809A0351	47D BEIVE	B.Tech	IA					
55	1809A0352	48VDRISHLI MVZHVVALH	B.Tech	IA					
56	1809A0353	49D AVISH VIVVA	B.Tech	IA					
57	1809A0354	50D BEIVE	B.Tech	IA					

*[Signature]*  
Principal

**PRINCIPAL**  
Global Institute of Engg. & Tech  
Chilukur (V), Moinabad (M)  
R. R. Dist

*[Signature]*  
Head of the Department

**HEAD**  
Department of Electrical & Electronics Engg.  
Global Institute of Engineering & Technology  
Chilukur (V), Moinabad (M), R.R. Dist, T.S.-591404.

VALLEADUTISEGHERI

SR - 20, Yelamanchili

బాబా ఎలక్ట్రానిక్స్ సాంస్కృతిక విజ్ఞాన కేంద్రం

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వల్లెఆడుతిశిగిరి

Department of Electrical and Electronics Engineering

VALLEADUTISEGHERI

SR - 20, Yelamanchili

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## DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

A Three Days Refresher Course

On

Power Electronics converters and it's Application  
(28<sup>th</sup> -30<sup>th</sup> August 2017)

### CERTIFICATE OF PARTICIPATION

This is to certify that Ms./Mr. Madishetti Prashanth bearing roll number 1SV61A0205

has attended A Three Days Course on "Power Electronics converters and it's Application " held from 28<sup>th</sup> August 2017 to 30<sup>th</sup> August 2017 at Global Institute of Engineering and Technology.

  
Co Ordinator  
(Mr.Issac)

  
Head of the Department  
(Mr.T.V.V Pavan Kumar)

  
Principal  
(Dr.V.V.V.S Murthy)

**REPORT**  
**ON**  
**THREE DAYS REFRESHER COURSE**  
**Power Electronics converters and its Application**

A Three Days Certificate course on **Power Electronics converters in Electrical and Electronics Engineering** was organized by the Department of Electrical and Electronics Engineering. Three days certificate course was held in the Conference Room of GIET from 28<sup>th</sup> August 2017 to 30<sup>th</sup> August 2017 and its objective was to further enhance and strengthen the technical skills of the students in the field of Power electronic converters. The course was attended by 11 students of Electrical and Electronics Engineering Department (A.Y. 2017-18). Evaluation forms were also filled by each participant at the end of the course.

**Day 1:**

The course started off with the inaugural ceremony and the Co ordinator of this occasion, Mr.Issac, Assistant Professor, and Department of EEE welcomed the gathering. Later in the inauguration Mr G Venu, CVSR, Hyderabad, delivered his inaugural and introductory speech at 10.00 AM in which he delivered his energetic talk on Introduction to ‘ Power Electronic converters and its Application’. He emphasized with a brief lecture on the field of Power Electronic converters and he is given extensive lecture on the converters. It is useful to the student’s those interested in the field of power Electronic converters. He is started with the fundamental in power Electronic and covered all the major topics in power electronics. He Provided the PPTs for the lecture and it is helpful for the students for their academics and also for the carrier. The total entire day he shared wide experience on power Electronic operation and control.

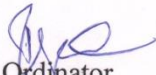


**Day 2:**

On Second day, first session was taken by Mr.G.Rajesh, Assistant Professor, GIET; Sir delivered a lecture on Power Electronics from 9:30 AM to 11:00AM. It was followed by another lecture by sir on day to day power demand in the present days from 11:15 AM to 1:00 PM, in which he elaborated on understanding of enhancements to Power Systems by using power electronic devices. In the first session, Power Electronics converters and it's Application. Secondly, Construction of **Power** Electronics converters is explained with the illustrative examples. Finally, the Applications of power electronics and real challenges in the power electronics industry is facing.

**Day 3:**

On third day, first session started at 9:30 AM on Applications of power Electronic Converters by Mr G Venu, CVSR, Hyderabad, and Dept of EEE. In which he spoke about the power Electronic Converters and different types of Converters which used in industrial and also sir focused on the current research on the power Electronic Converters. For this, Sir is given extensive lecture on the multilevel inverters with multiple conditions and also covered the recent trends in the power electronic converters. He is explained about what are the present research is going on in the Power electronic converters. It is very useful to students those are interested in research in Power electronic converters as well as academics.

  
Co- Ordinator  
Mr. Issac,  
Department of EEE