

ABOUT INSTITUTION:

Global Institute of Engineering and Technology, Hyderabad, Telangana is affiliated to JNTUH & approved by AICTE. GIET was established in the year 2006 with a vision. To produce technologically skilled Engineers of world class competency to address global challenges .GIET nurtures and supports a unique system of education structure on values and combines the tenets of the academic excellence with corporate professionalism. The primary objective at Global Institute of Engineering and Technology is to impart quality education which leads to quality living. Located at Moinabad, the sprawling campus spread over 28 acres of lush greenery creates the most conducive environment for education. GIET offers under graduate programmes in ECE ,EEE ,CSE, CE, ME ,CSE(AIML), CSE(DS)* and post graduate programmes Embedded Systems & Structural Engineering.

The system Of educational Global Institute of Engineering & Technology grooms the students to rise into professionals and leaders of high academic caliber with unblemished character, Dedicated ,experienced and qualified faculty help in honing the skills of students at early level of their learning.

ABOUT THE ECE DEPARTMENT:

The Department has good infrastructural facilities and is equipped with full-fledged laboratories. The Department also has audio-visual teaching aids with LCD for effective teaching. Keeping pace with the Global Trends GIET has established advanced labs in Digital Electronics, Mobile Communication ,Embedded Systems ,VLSI Design, Microwave Engineering etc. Main thrust of ECE Department is to provide better quality education required by the present industry and train the students in new technologies to equip them to cater to the present global requirements. The Department is offering M.Tech in Embedded Systems.

*Waiting for approval from JNTU

ABOUT THE CERTIFICATE COURSE:

This course elucidates concepts related to PCB fabrication is the process or procedure that transforms a circuit board design into a physical structure based upon the specifications provided in the design package. This physical manifestation is achieved through the following actions or techniques: Imaging desired layout on copper clad laminates.

Speakers:

- Mrs.Nuzath Unnisa Begum
Asst. Professor,ECE ,GIET
- Mr. Ahmed
Zeeshan Asst.
Professor and Head
ECE ,GIET.
- Mr. Indranil Gupta,
Technical Engineer,Coreel
Technologies Pvt.Ltd,Hyd.

TOPICS TO BE COVERED:

- Introduction to PCB layout
- Description of Electronic Component
- Technologies, Materials, Software tools in PCB Design
- PCB fabrication process
- Testing and Trouble shooting

Below are the means to help you with your self-reflection, self-verification:

1. **Attendance:** This Certificate Course is like a sentence spoken over 6 days. With Hands on. Participation in all sessions is desirable to get all the proposals being put forth.
2. **Assignments:** There will be session wise assignment which need to be submitted.
3. **Course Outcome Evaluation:**
Exam will be Conducted on last of Course for which you need to get 80% in order to receive Certificate.
4. **Feedback**

ORGANIZING COMMITTEE:

Chief Patron:

Mr. Arif uddin
M.Phil
Secretary MEWS

Patron:

1. Mr. KM Fasih uddin,
Director GPC

Co-Patrons:

1. Prof. Ravindra Tiwari,
Dean GPC
2. Prof. K Sivalingam,
Principal GIET

Organizing Secretary:

Mr. G. Ahmed Zeeshan
BE., M.Tech. (Ph.D.), LMISTE
Head ECE

Committee Members:

- | | |
|-----------------------------|-------------------------|
| 1. Dr. S. Padam Priyal | Prof, Dept of ECE |
| 2. Dr. Ch. Venkateswara Rao | Prof, Dept of ECE |
| 3. Mrs. Nuzath Unnisa Begum | Asst. Prof, Dept of ECE |
| 4. Mr. D Kamalakar Reddy | Asst. Prof, Dept of ECE |
| 5. Mr. Faizur Rahman Babul | Asst. Prof, Dept of ECE |
| 6. Ms. Imrose Basha | Asst. Prof, Dept of ECE |
| 7. Mrs. Anwar Jahan | Asst. Prof, Dept of ECE |
| 8. Ms. M Sai Chandana | Asst. Prof, Dept of ECE |
| 9. Mr. G Uday Kumar | Asst. Prof, Dept of ECE |
| 10. Mr. G Hari krishna | Asst. Prof, Dept of ECE |

How to Apply:

Interested students are required to fill Registration Form online only through Google forms at the link given below

<https://forms.gle/1nkUqovXxDSpaGH89>

Registration should be done between
01/01/2018 to 02/01/2018
Limited Registrations. First Come First
Serve

WHO SHOULD ATTEND?

The Course is designed for B.Tech students of
ECE

COORDINATOR:

Mrs. Ishrath Unisa
Asst. Prof Dept of ECE
Mobile: 9985951957

GLOBAL INSTITUTE OF ENGINEERING & TECHNOLOGY



A Workshop On

“PCB Design and Fabrication”
(03rd-04th January, 2018)



Organized by

DEPARTMENT OF ELECTRONICS &
COMMUNICATION ENGINEERING
GLOBAL INSTITUTE OF ENGINEERING
& TECHNOLOGY

(A NAAC Accredited Institution, Approved by
AICTE & Affiliated to JNTUH)
Recognized under section 2(f) of UGC Act 1956
An ISO 9001:2015 Certified Institution
Chilkur (V), Moinabad (M), RR Dist – TS
Phone No: +91-9985-359-601
+91-879-010-1015 +91-919959250205

E-mail:

principal.giet.u6@gmail.com gieteng@gmail.com
Website : www.globalhvd.edu.in





GLOBAL INSTITUTE OF ENGINEERING & TECHNOLOGY

(Approved by AICTE & Affiliated to JNTUH)

Survey No. 179, Chilkur (V), Moinabad (M), Ranga Reddy Dist. TS.

Phone: 8790101015/9959250205

e-mail: principal_giet.u6@gmail.com

JNTUH Code(U6) CIVIL – CSE – MECH – ECE – EEE – MBA – M.Tech. EAMCET Code-GLOB

Department of Electronics and Communication Engineering

G Ahmed Zeeshan

BE., M.Tech., (Ph.D)

MISTE., MISOL., MIEEE (USA)

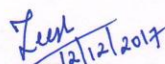
Assistant Professor & Head

Date: 12/12/2017

CIRCULAR

The Department of Electronics and Communication Engineering is hosting for *A Two Day Workshop on PCB Design & Fabrication* for the students of B. Tech.(ECE) in Room No. 04 on 03rd January 2018 & 04th January 2018. All the students of B.Tech (ECE) are hereby informed to register for the course on or before 02nd January 2018. The Co-Ordinator for the course is Mrs.Ishrath Unisa, Assistant Professor, Department of ECE.

Cc to:
Principal
Co ordinator IQAC
Mentors
Training & Placement Cell
Notice board
File


HEAD
Department of Electronics & Communication Eng
Global Institute of Engineering & Technolog
Chilkur (V), Moinabad (M), R.R. Dist.T.S.-50150



GLOBAL INSTITUTE OF ENGINEERING & TECHNOLOGY

(Approved by AICTE & Affiliated to JNTUH)

SURVEY NO 179, CHILKUR(V), MOINABAD(M), RANGAREDDY DIST, T.S

DEPARTMENT OF ELECTRONICS AND COMMUNICATION
ENGINEERING

A Two Day Workshop
On

PCB Design & Fabrication

03rd & 04th January, 2018)

PROGRAMME SCHEDULE

	TIME	SESSION	SPEAKER
Day 1	9:30 AM - 10:00 AM	Inauguration	
	10:00 AM - 11:00 AM	Introduction to PCB	Mr. Ahmed Zeeshan, Asst. Professor ECE, GIET
	11:00 AM - 11:30 AM	HIGH TEA	
	11:30 AM - 01:00 PM	PCB Designing Process	Prof. Srinivasulu, Professor of ECE, CBIT
	01:00 PM - 02:00 PM	LUNCH BREAK	
	02:00 PM - 03:30 PM	Technologies, Materials, Software tools in PCB Design	Mr. Vinay Kumar Design Engineer, Nexoft Pvt. Ltd., Hyd.
Day 2	TIME	SESSION	SPEAKER
	09:30 AM - 11:00 AM	PCB Manufacturing Process	Dr. Ravi Kumar, Professor of ECE, Siddhartha Inst. of Engg. & Tech.
	11:00 AM - 11:15 AM	TEA BREAK	
	11:15 AM - 01:00 PM	Multilayer PCB	Dr. Ravi Kumar, Professor of ECE, Siddhartha Inst. of Engg. & Tech.
	01:00 PM - 02:00 PM	LUNCH BREAK	
	02:00 PM - 03:30 PM	Valedictory Function, Feedback and Certificate Distribution	

Co- Ordinator
Mrs. Ishrath Unisa
Assistant Professor
Department of ECE



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DEPARTMENT OF ELECTRONICS AND COMMUNICATION
ENGINEERING

A Two Day Workshop

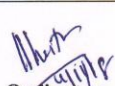
On

PCB DESIGN & FABRICATION

(03rd & 04th January, 2018)

LIST OF PARTICIPANTS

S. NO:	NAME OF THE PARTICIPANT
1.	VEM VAMSHI
2.	AFREEN BEGUM
3.	ASRA FARDEEN
4.	BADDAMOLLA SHIREESHA
5.	BOGULAPALLY KARTHIK KUMAR REDDY
6.	CHIDDANNA REVANTH
7.	DESIBOYINA BHARGAV SAI
8.	JANGAM VIKAS KUMAR
9.	KAUSER BEGUM
10.	MOHD KAZIM SHARIF
11.	N SOWMYA
12.	NALLA POOJA REDDY
13.	NANDAMURI NARENDRA
14.	PANDAOULA SHASHIKUMAR
15.	SHEELAM RAMAKRISHNA
16.	SUMMAIYA
17.	YANAGANDULA SURESH
18.	A YAMUNA
19.	KARUPURAM BHAVANA
20.	MOHAMMED ABDUL BARI KHAN
21.	POTTA RAGHAVENDER
22.	SANKOJI KRISHNAVENI
23.	SANTHAPURAM NIKHIL


Co-Ordinator
Mrs. Ishrath Unisa
Assistant Professor
Department of ECE



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SURVEY NO 179, CHILKUR(V), MOINABAD(M), RANGAREDDY DIST, T.S

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

A Two Day Workshop

On

PCB Design & Fabrication
(03rd & 04th January, 2018)

REGISTRATION FORM

(Please Fill in CAPITAL LETTERS)

Name : KARUPURAM BHAVANA

Roll No : 17U65A0402

Course/Year : B.TECH/II

Department : ECE

E-mail : Krishnamudhiraj1964@gmail.com

Phone No : 6302389431

K. Bhavane
Signature of Participant

For Office use only

Approved/ Not Approved

Signature of Coordinator



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SURVEY NO 179, CHILKUR(V), MOINABAD(M), RANGAREDDY DIST, T.S

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
A Two Day Workshop

On

PCB Design & Fabrication
(03rd & 04th January, 2018)
ATTENDANCE SHEET

S.No.	Name of the Participant	Programme	Year	Signature			
				Day-1 (03/01/2018)	Day-2 (04/01/2018)	FN	AN
1.	VEM VAMSHI	B.Tech	II	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>
2.	AFREEN BEGUM	B.Tech	II	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>
3.	ASRA FARDEEN	B.Tech	II	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>
4.	BADDAMOLLA SHIREESHA	B.Tech	II	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>
5.	BOGULAPALLY KARTHIK KUMAR REDDY	B.Tech	II	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>

Co-Ordinator

Head of the Department

Department of Electronics & Communication Engg.
Global Institute of Engineering & Technology
Chilkur (V), Moimabad (M), R.R. Dist. T.S-501562.



Principal
Global Institute of Engg. & Tech
Chilkur (V), Moimabad (M)
R. R. Dist



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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

A Two Day Workshop

On

PCB Design & Fabrication
(03rd & 04th January, 2018)

ATTENDANCE SHEET

S.No.	Name of the Participant	Programme	Year	Signature			
				Day-1 (03/01/2018)	Day-2 (04/01/2018)		
6.	CHIDDANNA REVANTH	B.Tech	II	FN	AN	FN	AN
7.	DESIBOYINA BHARGAV SAI	B.Tech	II	Revanth	Revanth	Revanth	Revanth
8.	JANGAM VIKAS KUMAR	B.Tech	II	Revanth	Revanth	Revanth	Revanth
9.	KAUSER BEGUM	B.Tech	II	Revanth	Revanth	Revanth	Revanth
10.	MOHD KAZIM SHARIF	B.Tech	II	Revanth	Revanth	Revanth	Revanth

Co Ordinator

Head of the Department

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Principal
Global Institute of Engg. & T.
Chilkur (V), Moinsabad (M)
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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

A Two Day Workshop

On

PCB Design & Fabrication
(03rd & 04th January, 2018)

ATTENDANCE SHEET

S.No.	Name of the Participant	Programme	Year	Signature			
				Day-1 (03/01/2018)		Day-2 (04/01/2018)	
11.	N SOWMYA	B.Tech	II	FN	AN	FN	AN
12.	NALLA POOJA REDDY	B.Tech	II	N. pooja	N. pooja	N. pooja	N. pooja
13.	NANDAMURI NARENDRA	B.Tech	II	Pst	Pst	Pst	Pst
14.	PANDAOULA SHASHIKUMAR	B.Tech	II	Shashi	Shashi	Shashi	Shashi
15.	SHEELAM RAMAKRISHNA	B.Tech	II	ela	ela	ela	ela

Co Ordinator

Head of the Department

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(03rd & 04th January, 2018)

ATTENDANCE SHEET

S.No.	Name of the Participant	Programme	Year	Signature			
				Day-1 (03/01/2018)	Day-2 (04/01/2018)		
16.	SUMMAIYA	B.Tech	II	FN	AN	FN	AN
17.	YANAGANDULA SURESH	B.Tech	II	AN	AN	AN	AN
18.	A YAMUNA	B.Tech	II	AN	AN	AN	AN
19.	KARUPURAM BHAVANA	B.Tech	II	AN	AN	AN	AN
20.	MOHAMMED ABDUL BARI KHAN	B.Tech	II	AN	AN	AN	AN

Co-Ordinator

Head of the Department
Department of Electronics & Communication Engg.
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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

A Two Day Workshop

On

PCB Design & Fabrication
(03th & 04th January, 2018)

ATTENDANCE SHEET

S.No.	Name of the Participant	Programme	Year	Signature			
				Day-1 (03/01/2018)		Day-2 (04/01/2018)	
				FN	AN	FN	AN
21.	POTTA RAGHAVENDER	B.Tech	II	<i>Raghu</i>	<i>Raghu</i>	<i>Raghu</i>	<i>Raghu</i>
22.	SANKOLI KRISHNAVENI	B.Tech	II	<i>Sankoli</i>	<i>Sankoli</i>	<i>Sankoli</i>	<i>Sankoli</i>
23.	SANTHAPURAM NIKHIL	B.Tech	II	<i>Nikhil</i>	<i>Nikhil</i>	<i>Nikhil</i>	<i>Nikhil</i>

Co Ordinator

Head of the Department

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R. R. Dist



GLOBAL INSTITUTE OF ENGINEERING & TECHNOLOGY

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Phone: 8790101015 / 9959250205

e-mail: principal.giet.u6@gmail.com

JNTUH Code(U6) CIVIL – CSE – MECH – ECE – EEE – MBA – M.Tech. EAMCET Code-GLOB

Department of Electronics and Communication Engineering

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

A Two Day Workshop

On

Machine Learning

(3rd & 4th January, 2018)

FEEDBACK FORM

Please valuate your rating of the course by placing a tick in the appropriate box.

1. Poor 2. Satisfactory 3. Good 4. Very good 5. Excellent

Branch and Year: ECE 2nd yrs

Date: 4/1/18


ASPECTS	RATING				
	Excellent 5	Very good 4	Good 3	Satisfactory 2	Poor 1
Relevance of contents	<input checked="" type="checkbox"/>				
Trainer was knowledgeable and skillful	<input checked="" type="checkbox"/>				
Quality of input provided	<input checked="" type="checkbox"/>				
Quality of presentations	<input checked="" type="checkbox"/>				
Adherence to the time schedule	<input checked="" type="checkbox"/>				
Opportunity given to participant to clear doubts	<input checked="" type="checkbox"/>				
Identify ways to build on current skills and knowledge	<input checked="" type="checkbox"/>				
Overall learning experience	<input checked="" type="checkbox"/>				
How has the course enhanced your skills or understanding of this topic?					
<u>Excellent</u>					
Specify problems faced by you during the course?					
<u>✓</u>					

**REPORT
ON
TWO DAY WORKSHOP:
PCB DESIGN & FABRICATION
HELD AT
GLOBAL INSTITUTE OF ENGINEERING AND
TECHNOLOGY, HYDERABAD
(03rd & 04th January, 2018)**

A Two day Workshop titled "PCB Design & Fabrication" was organized by department of Electronics & Communication Engineering OF GIET, HYDERABAD the workshop commenced with inaugural ceremony at 9:30 AM Mr.G.Ahmed Zeeshan, Assistant Professor ECE, GIET. Welcomed all the dignitaries present on the dais and colleagues & students. He mentioned about the significance of the workshop. Prof. Srinivasulu, Professor of ECE, CBIT described the importance of PCB in electronics and communication engineering and how they are interconnected from a long time. He motivated everyone to actively participate in the workshop to get benefited.

Day 1: The Technical session started at 10:00 AM, Mr.G.Ahmed Zeeshan, Assistant Professor ECE, GIET. Delivered a topic on "Introduction to PCB". He discussed about evolution of PCB and its various applications. Prof. Srinivasulu, Professor, CBIT explained the "PCB Designing Process" in which sir explained about various designing methodologies of PCB from 11:30 AM to 1:00 PM. The next speaker Mr. Vinay Kumar, Design Engineer, Nexoft Pvt.Ltd., Hyd lectured on "Technologies, Materials, Software tools in PCB Design" which includes different technologies used for designing of PCB. He also discussed the types of substrates used and dielectric constants and various tools from 2:00 PM to 3:30 PM.

Day 2: Session started at 9:30 am, Dr. Ravi Kumar, Professor of ECE, Siddhartha Inst. of Engg. & Tech. delivered two lectures on "PCB Manufacturing process" & "Multilayer PCB". He discussed about PCB Manufacturing steps and explained each step with its schematic diagram in first session. Followed by the Multilayer PCB and its uses in second session. Both the sessions gives hands on experience of PCB Fabrication process. The workshop concludes with validity function, feedback and certificate distribution.


Co Ordinator
Mrs. Ishrath Unisa
Assistant Professor
Department of ECE



GLOBAL INSTITUTE OF ENGINEERING & TECHNOLOGY

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

A Two Day Workshop
On

PCB DESIGN & FABRICATION
(03rd & 04th January 2018)

CERTIFICATE OF PARTICIPATION

This is to certify that Ms./Mr.

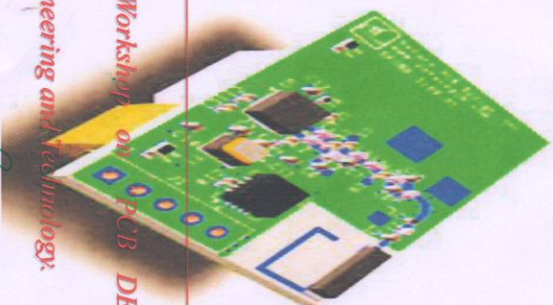
KARUPURAM BHAVANA


bearing roll number


17U65A04102

has attended a Two Day Workshop on **PCB DESIGN &**

FABRICATION" held on 03rd January 2018 & 04th January 2018 at Global Institute of Engineering and Technology.




Co-Ordinator
(Ishrath Unisa)


Head of the Department
(G. Ahmed Zeeshan)


Principal
(Dr. V. V. V. S Murthy)



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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

A Two Day Workshop

On

PCB DESIGN & FABRICATION

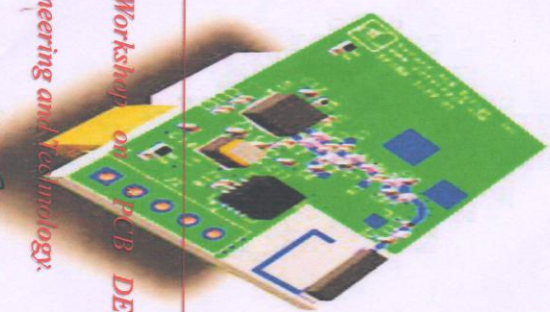
(03rd & 04th January 2018)

CERTIFICATE OF PARTICIPATION


This is to certify that Ms./Mr. AFREEN BEGUM

bearing roll number 16U61A0401 has attended a Two Day Workshop on **PCB DESIGN &**

FABRICATION held on 03rd January 2018 & 04th January 2018 at Global Institute of Engineering and Technology.




Co-Ordinator
(Ishrath Unisa)


Head of the Department
(G. Ahmed Zeeshan)


Principal
(Dr. V. V. S. Murthy)



GLOBAL INSTITUTE OF ENGINEERING & TECHNOLOGY

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

A Two Day Workshop

On

PCB DESIGN & FABRICATION
(03rd & 04th January 2018)

CERTIFICATE OF PARTICIPATION

This is to certify that Ms./Mr.

A YAMUNA

bearing roll number

17U65A0401

has attended a Two Day Workshop on PCB DESIGN &

FABRICATION" held on 03rd January 2018 & 04th January 2018 at Global Institute of Engineering and Technology.

Co-Ordinator
(Ishrath Unisa)

Head of the Department
(G. Ahmed Zeeshan)

Principal
(Dr. V. V. V. S Murthy)

